

# GSM3660EX7F

## 30V N-Channel MOSFET

### Product Description

GSM3660E, N-Channel enhancement mode MOSFET, uses Advanced Trench Technology to provide excellent  $R_{DS(ON)}$ , low gate charge.

These devices are particularly suited for low voltage power management, such as smart phone and notebook computer, and low in-line power loss are needed in commercial industrial surface mount applications.

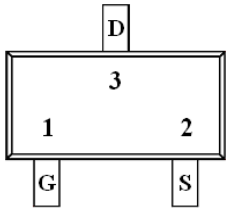
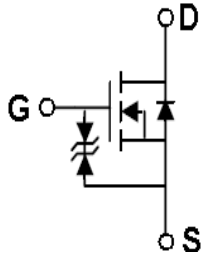
### Features

- Low Gate Charge
- ESD Protected
- SOT-523 Package
- RoHS Compliant and Halogen Free

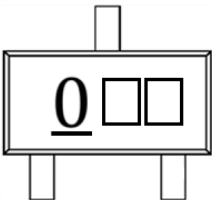
### Applications

- MB / VGA / Vcore
- POL Applications
- Load Switch
- LED Application

### Package & Pin Assignment

GSM3660EX7F (SOT-523)			Equivalent Circuit
			
Pin	Symbol	Description	
1	G	Gate	
2	S	Source	
3	D	Drain	

## Ordering and Marking Information

Ordering Information			
Part Number	Package	Part Marking	Quantity / Reel
GSM3660EX7F	SOT-523	0 □ □	3,000 PCS
<b>GSM3660E</b> 1 2			
<div> <div> <b>- Product Code:</b> GSM3660E           </div> <div> <b>- Package Code:</b> 1 is X7 for SOT-523           </div> <div> <b>- Green Level:</b> 2 is <b>F</b> for RoHS Compliant and Halogen Free           </div> </div>			
Marking Information			
<div> <div>  </div> <div> <b>- Product Code:</b> 0           </div> <div> <b>- GS Code:</b> □ □           </div> </div>			

## Absolute Maximum Ratings (T<sub>A</sub> = 25°C unless otherwise specified)

Symbol	Parameter	Value	Unit
V <sub>DS</sub>	Drain-Source Voltage	30	V
V <sub>GS</sub>	Gate-Source Voltage	±12	V
I <sub>D</sub>	Continuous Drain Current	T <sub>A</sub> =25°C	A
I <sub>DM</sub>	Pulsed Drain Current <sup>1,2</sup>	2	A
P <sub>D</sub>	Power Dissipation	T <sub>A</sub> =25°C	W
R <sub>θJA</sub>	Thermal Resistance - Junction to Ambient	450	°C/W
T <sub>J</sub>	Operating Junction Temperature Range	-55 to +150	°C
T <sub>STG</sub>	Storage Temperature Range	-55 to +150	°C

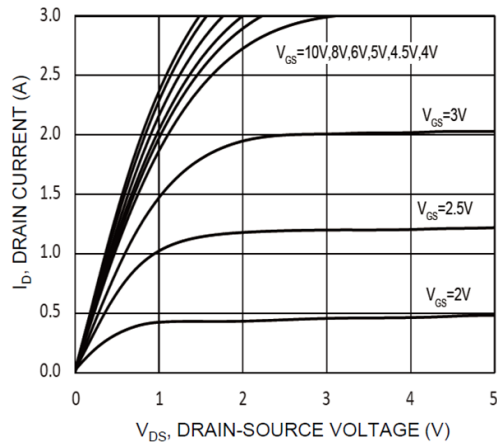
### Notes:

1. Surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2oz copper.
2. Pulse width limited by maximum junction temperature, Pulse Width≤300μs, Duty Cycle≤1%.

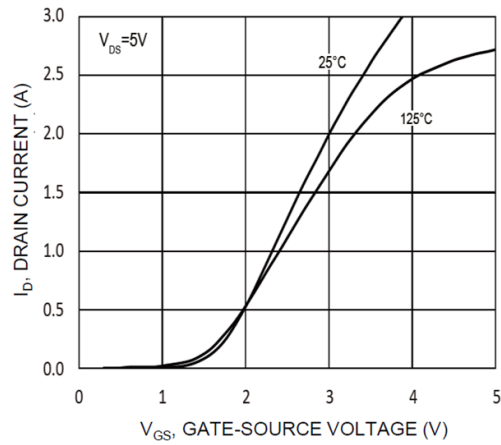
## Electrical Characteristics (T<sub>A</sub> = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static characteristics						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	30			V
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	0.5		1.5	V
I <sub>GSS</sub>	Gate Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> =±12V			10	nA
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =24V, V <sub>GS</sub> =0V			100	uA
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =0.5A		355	600	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =0.4A		435	650	
		V <sub>GS</sub> =2.5V, I <sub>D</sub> =0.3A		665	1200	
g <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> =10V, I <sub>D</sub> =0.5A		1.1		S
V <sub>SD</sub>	Diode Forward Voltage	I <sub>S</sub> =0.25A, V <sub>GS</sub> =0V			1.35	V
Gate charge characteristics						
Q <sub>g</sub>	Total Gate Charge <sup>3,4</sup>	V <sub>DS</sub> =15V, V <sub>GS</sub> =10V, I <sub>D</sub> =0.5A		1.5		nC
Q <sub>gs</sub>	Gate-Source Charge <sup>3,4</sup>			0.2		
Q <sub>gd</sub>	Gate-Drain Charge <sup>3,4</sup>			0.2		
Dynamic characteristics						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V f=1MHz		39		pF
C <sub>oss</sub>	Output Capacitance			9		
C <sub>rss</sub>	Reverse Transfer Capacitance			6		
t <sub>d(on)</sub>	Turn-On Time	V <sub>DD</sub> =15V, I <sub>D</sub> =0.5A, V <sub>GS</sub> =10V, R <sub>G</sub> =2.5Ω		5.3		ns
t <sub>r</sub>	Rise Time			16		
t <sub>d(off)</sub>	Turn-Off Time			20		
t <sub>f</sub>	Fall Time			18		

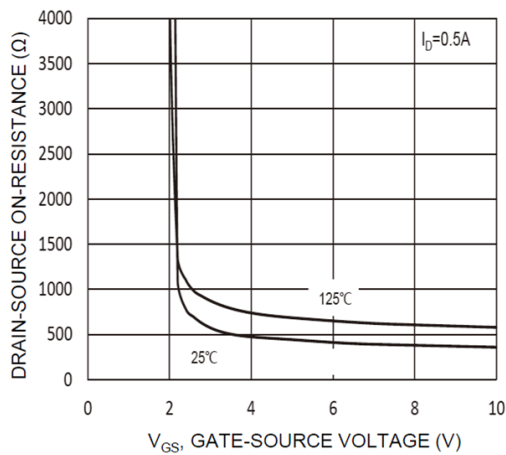
## Typical Performance Characteristics ( $T_A = 25^\circ\text{C}$ unless otherwise specified)



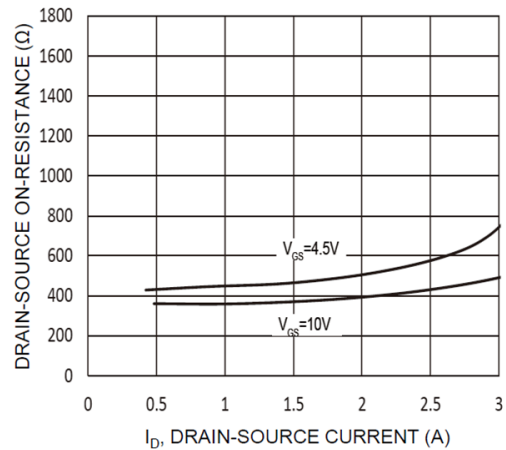
**Fig.1 Output Characteristics**



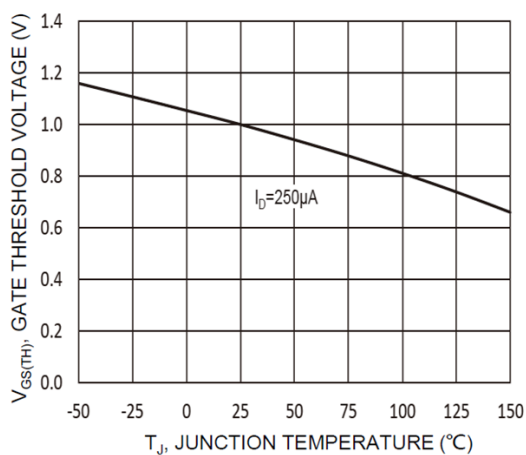
**Fig.2 Transfer Characteristics**



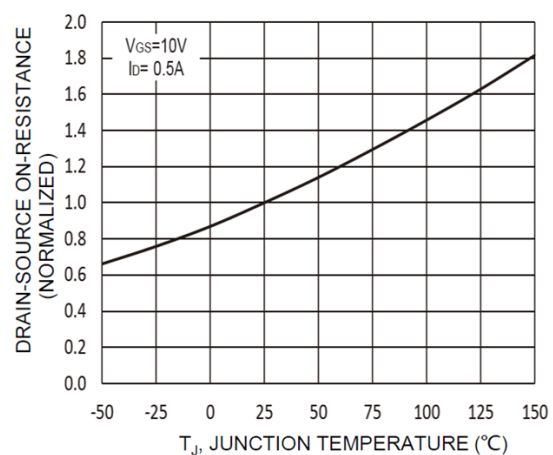
**Fig.3 On-Resistance vs.  $V_{GS}$**



**Fig.4 On-Resistance vs.  $I_D$**



**Fig.5 Normalized Threshold Voltage**



**Fig.6 On-Resistance vs.  $T_J$**

## Typical Performance Characteristics (Continued)

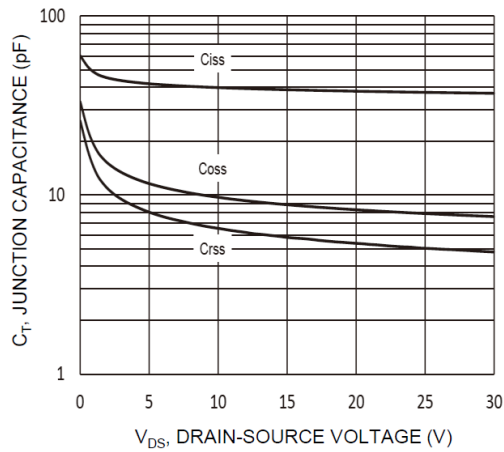


Fig.7 Capacitance

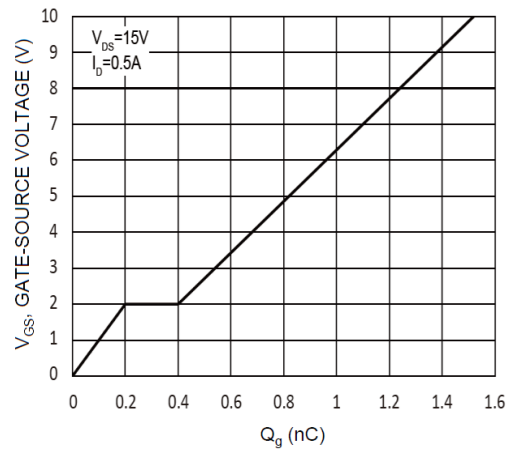


Fig.8 Gate Charge

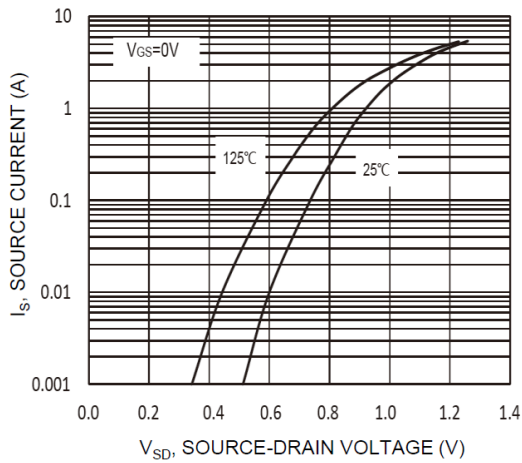


Fig.9 Diode Forward Voltage vs. Current

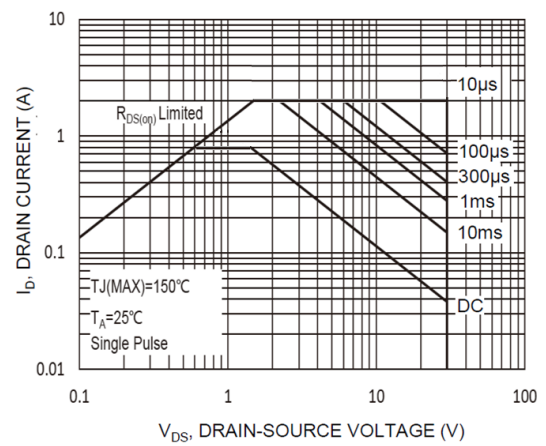


Fig.10 Safe Operation Area

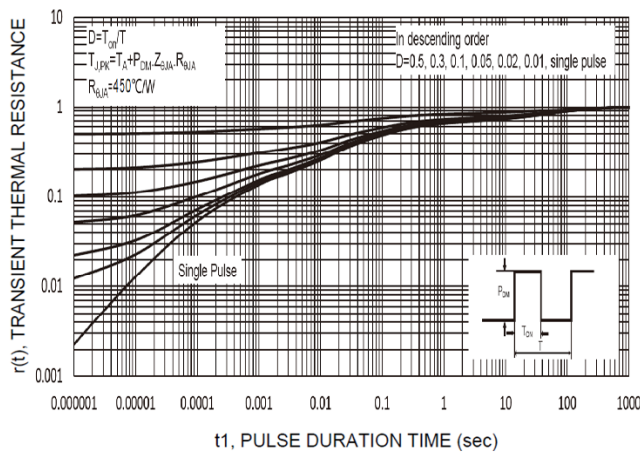
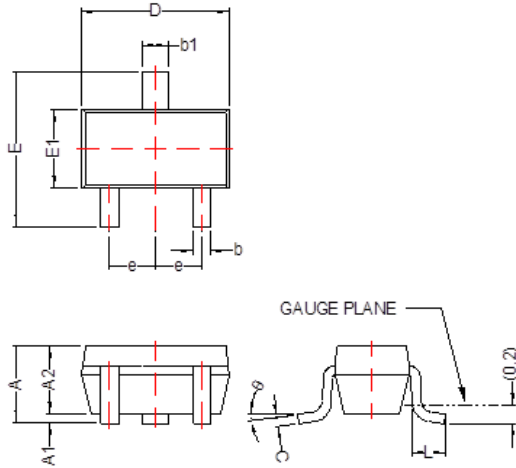


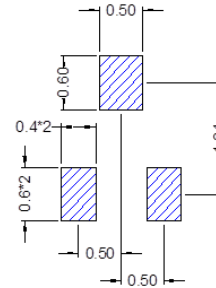
Fig.11 Normalized Transient Impedance

# SOT-523

## Package Dimension



## Recommended Land Pattern



Unit: mm

Dimensions				
Symbol	Millimeters		Inches	
	Min	Max	Min	Max
A	0.60	0.95	0.024	0.037
A1	0.00	0.10	0.000	0.004
A2	0.60	0.85	0.024	0.033
b	0.15	0.30	0.006	0.012
b1	0.25	0.40	0.010	0.016
c	0.08	0.25	0.003	0.010
D	1.40	1.80	0.055	0.071
E	1.40	1.80	0.055	0.071
E1	0.70	0.90	0.028	0.035
e	0.50 BSC		0.020 BSC	
L	0.26	0.46	0.010	0.018
θ	0°	8°	0°	8°





### NOTE:



Dimensions are exclusive of Burrs, Mold Flash and Tie Bar extrusions.

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