GSM2122JZF 20V N-Channel Enhancement Mode MOSFET

Product Description

GSM2122JZF, N-Channel enhancement mode MOSFET, uses Advanced Trench Technology to provide excellent $R_{DS(ON)}$, low gate charge.

The device is particularly suited for low voltage power management, such as smart phone and notebook computer and other battery powered circuits, and low in-line power loss are needed in commercial industrial surface mount applications.

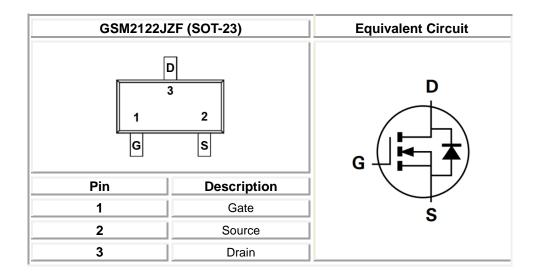
Features

- R_{DS(ON)}=22mΩ @ V_{GS}=4.5V
- Super high-density cell design for extremely low R_{DS (ON)}
- SOT-23 package
- RoHS Compliant and Halogen Free

Applications

- Portable Equipment
- Battery Powered System
- Net Working System

Packages & Pin Assignments





Ordering and Marking Information

Ordering Information					
Part Number	Package	Part Marking Quantity /			
GSM2122JZF	SOT-23	22	3,000 PCS		
GSM2122112					
- Product Code: GSM2122	 Package Code: 1 is JZ for SOT-23 Green Level: is F for RoHS Compliant and Halogen Free 				
	Marking Ir	nformation			
- Product Code: 22 - GS Code:					



Absolute Maximum Ratings (TA=25°C, unless otherwise specified)

Symbol	Parameter		Value	Unit	
VDSS	Drain-Source Voltage		20	V	
V _{GSS}	Gate-Source Voltage		±12	V	
		T _A =25°C	6	•	
Ι _D	Continuous Drain Current	T _A =70°C	5	A	
Ідм	Pulsed Drain Current		20	A	
P _D Power Dissipat		T _A =25°C	1.25		
	Power Dissipation	T _A =70°C	0.8	W	
TJ	Operating Junction Temperature		-55 to +150	°C	
Tstg	Storage Temperature Range		-55 to +150	°C	
Reja	Thermal Resistance, Junction to Ambient		100	°C/W	

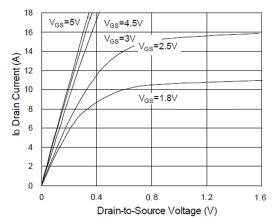
Electrical Characteristics (T_A=25°C, unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Static characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250µA	20	-	-	V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250µA	0.4	-	1	V
Igss	Gate Leakage Current	V _{DS} =0V, V _{GS} =±12V	-	-	±100	nA
IDSS	Zero Gate Voltage Drain Current	V _{DS} =20V, V _{GS} =0V	-	-	1	μA
	Drain-Source On-Resistance	V _{GS} =4.5V, I _D =4A	-	17	22	mΩ
RDS(ON)		V _{GS} =2.5V, I _D =3A	-	24	32	
		V _{GS} =1.8V, I _D =2A	-	35	45	
g fs	Forward Transconductance	V _{DS} =10V, I _D =3A	-	3.7	-	S
V _{SD}	Diode Forward Voltage	I _S =1A, V _{GS} =0V	-	-	1	V
	Dynami	c characteristics				
Ciss	Input Capacitance		-	560	-	pF
Coss	Output Capacitance	V _{DS} =10V, V _{GS} =0V, f=1MHz	-	166	-	
Crss	Reverse Transfer Capacitance	100-01, 1-11112	-	150	-	
Qg	Total Gate Charge ^{1 · 2}		-	8.5	-	
Q _{gs}	Gate-Source Charge ^{1 · 2}	V _{DS} =10V, V _{GS} =4.5V, I _D =6A	-	1.5	-	nC
Q _{gd}	Gate-Drain Charge ^{1 · 2}	VG5-7.0V, ID-0A	-	3.5	-	
t _{d(on)}	Turn-On Time ^{1 · 2}		-	12	-	
tr	Rise Time ^{1 · 2}	V _{DD} =10V, I _D =1A,	· ·	15	-	
td(off)	Turn-Off Time ^{1 · 2}	V _{GS} =4.5V, R _G =6Ω]	30	-	ns
tr	Fall Time ^{1 · 2}		-	15	-	

Note:

The data tested by pulsed, pulse width≤300us, duty cycle≤2%.
 Independent of operating temperature.







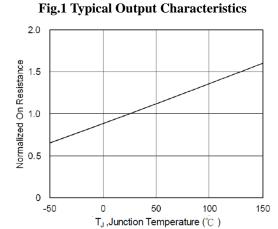


Fig.3 Normalized On-Resistance vs. TJ

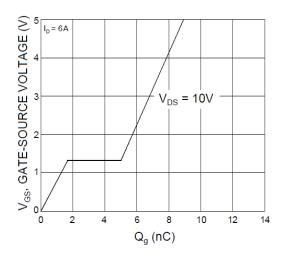


Fig.5 Gate Charge Characteristics

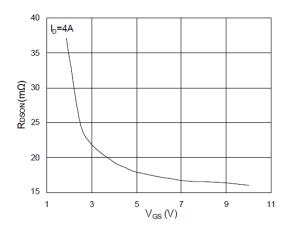


Fig.2 Typical On-Resistance vs. Gate Voltage

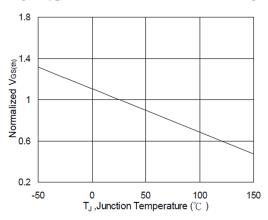


Fig.4 Normalized V_{GS(th)} vs. T_J

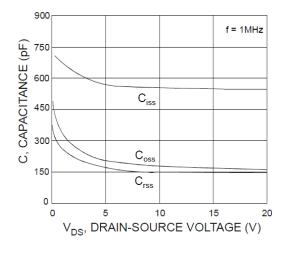


Fig.6 Capacitance Characteristics



Typical Performance Characteristics

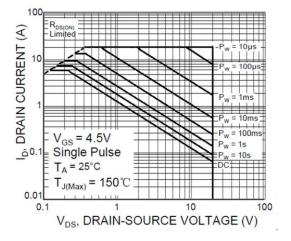


Fig.7 Maximum Safe Operation Area

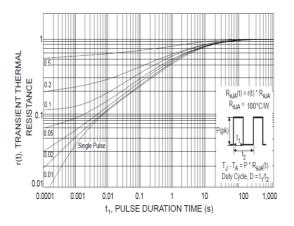
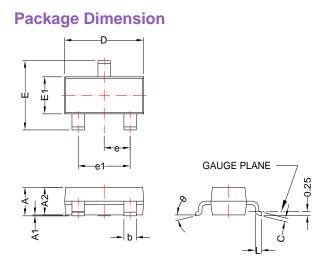


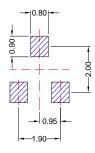
Fig.8 Normalized Transient Impedance

GSM2122JZF

SOT-23



Recommended Land Pattern



	Dimensions				
	Millimeters		Inches		
Symbol	MIN	МАХ	MIN	MAX	
Α	0.75	1.17	0.030	0.046	
A1	0.01	0.15	0.000	0.006	
A2	0.70	1.02	0.028	0.040	
b	0.30	0.50	0.012	0.020	
С	0.08	0.20	0.003	0.008	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E1	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.037	BSC	
e1	1.90 BSC		0.075	BSC	
L	0.3	0.6	0.012	0.024	
θ	0°	8°	0°	8°	

NOTE:

Dimensions are exclusive of Burrs, Mold Flash & Tie Bar extrusions.

-GSM2122JZF

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