

GSM02P15ZF

150V P-Channel MOSFET

Product Description

The P-Channel enhancement mode power field effect transistor is using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

The device is well suited for high efficiency fast switching applications.

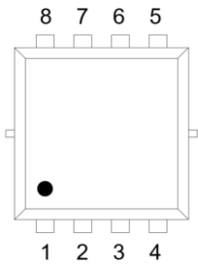
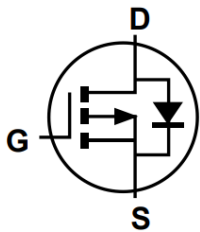
Features

- $R_{DS(ON)} = 780m\Omega @ V_{GS} = -10V$
- $R_{DS(ON)} = 950m\Omega @ V_{GS} = -6V$
- DFN3X3-8L Package
- RoHS Compliant and Halogen Free

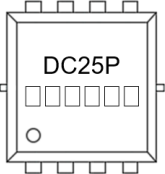
Applications

- LED Applications
- Load Switch

Packages & Pin Assignments

DFN3X3-8L			Equivalent Circuit		
					
Pin	Symbol	Description	Pin	Symbol	Description
1	S	Source	8	D	Drain
2	S	Source	7	D	Drain
3	S	Source	6	D	Drain
4	G	Gate	5	D	Drain

Ordering and Marking Information

Ordering Information			
Part Number	Package	Part Marking	Quantity / Reel
GSM02P15ZF	DFN3X3-8L	DC25P □□□□□□	5,000 PCS
GSM02P15 1 2			
- Product Code: GSM02P15		- Package Code: 1 is Z for DFN3X3-8L	- Green Level: 2 is F for RoHS Compliant and Halogen Free
Marking Information			
		- Product Code: DC25P	
		- GS Code: □□□□□□ •The Dot denotes Pin 1	

Absolute Maximum Ratings (T_A = 25°C unless otherwise specified)

Symbol	Parameter	Value	Unit	
V _{DSS}	Drain-Source Voltage	-150	V	
V _{GSS}	Gate-Source Voltage	±20	V	
I _D	Continuous Drain Current ¹	T _C =25°C	-2.18	A
		T _C =100°C	-1.37	
I _{DM}	Pulsed Drain Current ¹	-8	A	
P _D	Power Dissipation	T _C =25°C	7.8	W
		T _C =100°C	3.1	
R _{θJC}	Thermal Resistance-Junction to Case	16	°C/W	
T _J	Operating Junction Temperature Range	-55 to +150	°C	
T _{STG}	Storage Temperature Range	-55 to +150	°C	

NOTE:

1. Single pulse width is limited by max junction temperature.

Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
B _V DSS	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250μA	-150	-	-	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-150V, V _{GS} =0V	-	-	-1	μA
I _{GSS}	Gate-Source Leakage Current	V _{DS} =0V, V _{GS} =±20V	-	-	±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =-250μA	-2	-3	-4	V
R _{DS(ON)}	Drain-Source On-Resistance	V _{GS} =-10V, I _D =-1A	-	650	780	mΩ
		V _{GS} =-6V, I _D =-0.5A	-	700	950	
g _{fs}	Forward Transconductance	V _{DS} =-10V, I _D =-1A	-	2	-	S
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =-75V, V _{GS} =0V, f=1MHz	-	403	810	pF
C _{oss}	Output Capacitance		-	31	-	
C _{rss}	Reverse Transfer Capacitance		-	18	-	
Q _g	Total Gate Charge	V _{DS} =-75V, I _D =-1A V _{GS} =-10V	-	4.4	8	nC
Q _{gs}	Gate-Source Charge		-	0.7	-	
Q _{gd}	Gate-Drain Charge		-	1.5	-	
t _{d(on)}	Turn-On Delay Time	V _{DD} =-75V, I _D =-1A V _{GS} =-10V, R _g =10Ω	-	13	-	ns
t _r	Turn-On Rise Time		-	9	-	
t _{d(off)}	Turn-Off Delay Time		-	18	-	
t _f	Turn-Off Fall Time		-	12	-	
Diode Characteristics						
V _{SD}	Diode Forward Voltage	V _{GS} =0V, I _S =-1A	-	-	-1	V
t _{rr}	Reverse Recovery Time	V _{GS} =0V, I _S =1A , di/dt=100A/μs	-	70	-	ns
Q _{rr}	Reverse Recovery Charge		-	114	-	nC

Typical Performance Characteristics

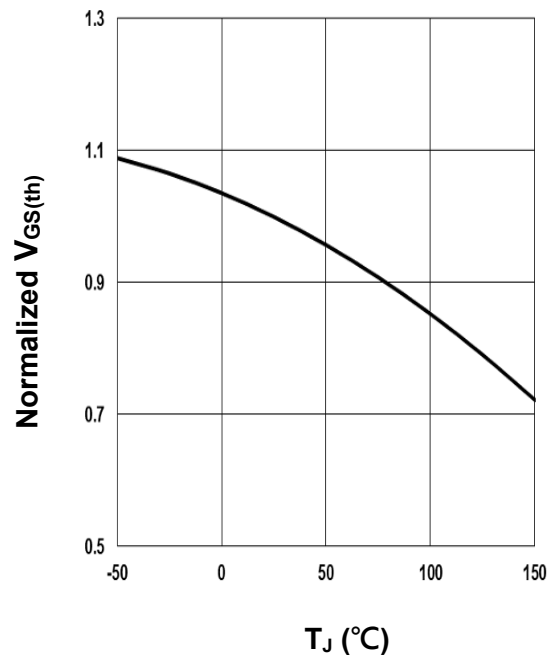
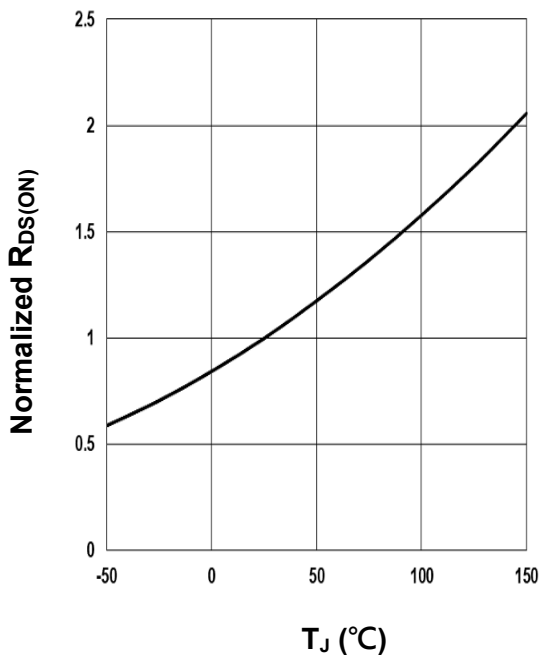
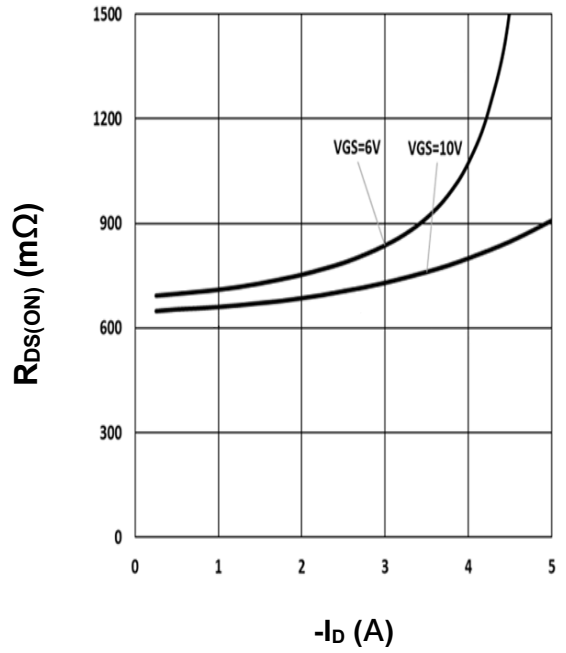
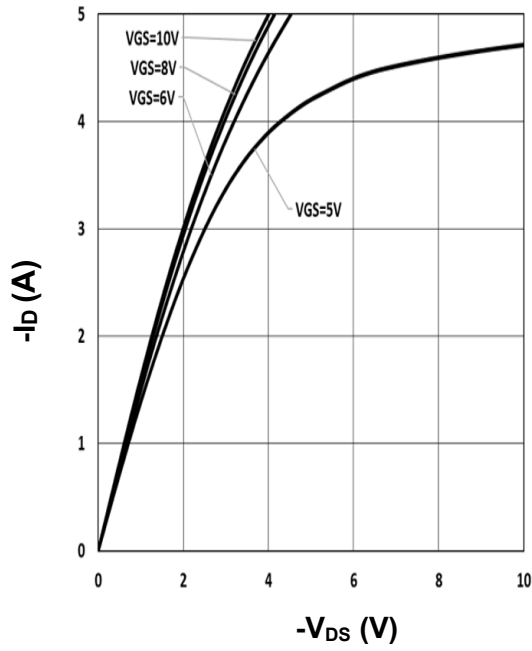
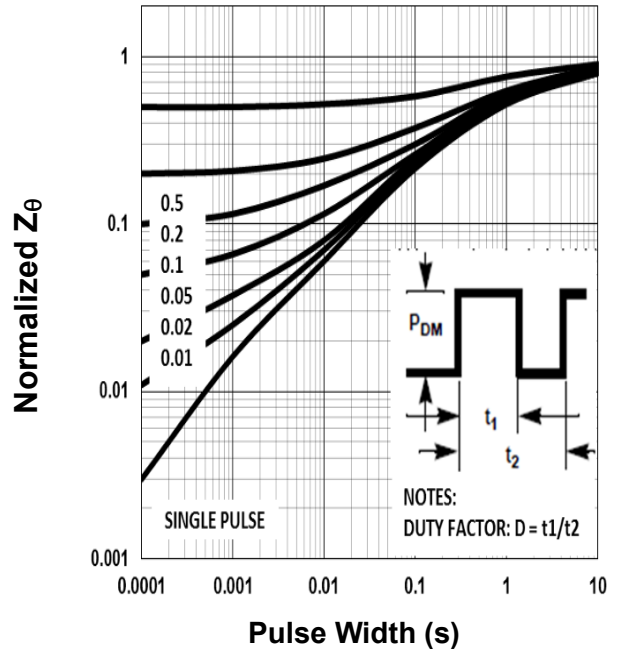
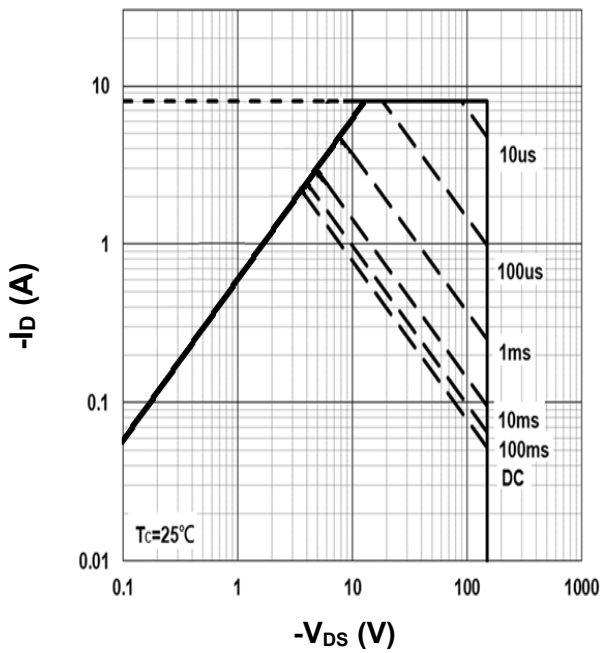
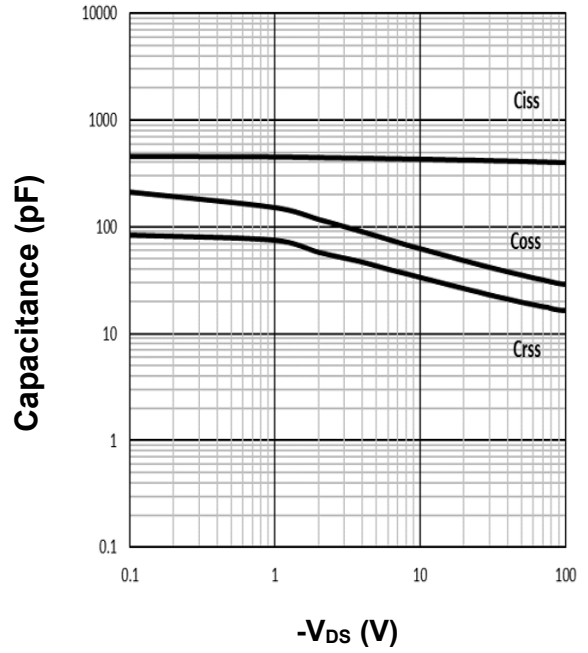
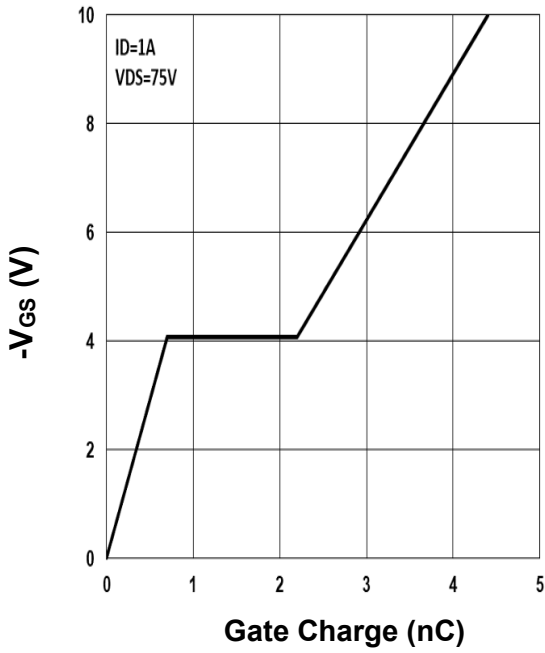


FIG.3 Normalized On-Resistance vs. T_J

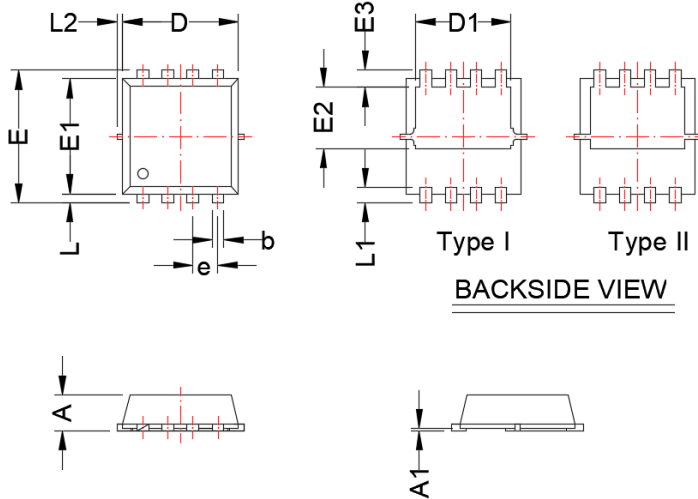
FIG.4 Normalized $V_{GS(th)}$ vs. T_J

Typical Performance Characteristics

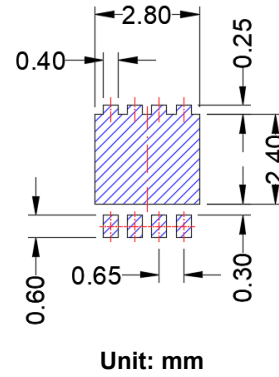


DFN3X3-8L

Package Dimension



Recommended Land Pattern



Unit: mm

Dimensions

Symbol	Millimeters		Inches	
	Min	Max	Min	Max
A	0.70	0.90	0.028	0.035
A1	0.00	0.05	0.000	0.002
b	0.24	0.37	0.009	0.015
c	0.10	0.25	0.004	0.010
D	2.90	3.25	0.114	0.128
D1	2.35	2.60	0.093	0.102
E	3.05	3.45	0.120	0.136
E1	2.90	3.20	0.114	0.126
E2	1.35	2.00	0.053	0.079
E3	0.30	0.60	0.012	0.024
e	0.65 BSC		0.026 BSC	
L	0.02	0.2	0.001	0.008
L1	0.28	0.5	0.011	0.020
L2	---	0.15	---	0.006





NOTE:



Dimensions are exclusive of Burrs, Mold Flash and Tie Bar extrusions.

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