

GS74LVC1G07 Series

Single BUFFER with Open-Drain Output

Product Description

The GS74LVC1G07 is designed for 1.65V to 5.5V V_{cc} operation, providing the non-inverting buffer.

Inputs can be driven from either 3.3V or 5V devices. This feature allows the use of this device in a mixed 3.3V and 5V system environment.

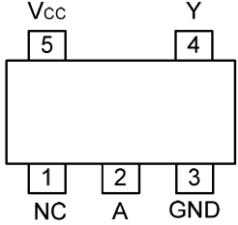
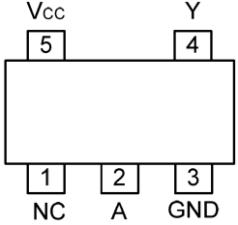
Schmitt trigger action at all inputs makes the circuit tolerant for slower input rise and fall time.

This device is fully specified for partial power-down applications using I_{OFF}. The I_{OFF} circuitry disables the output, preventing damaging backflow current through the device when it is powered down.

Features

- Supports 1.65V to 5.5V V_{cc} operation
- 24mA sink current at V_{cc}=3.0V
- CMOS low power consumption
- Direct interface with TTL levels
- Input accepts voltages up to 5V
- Latch-up performance exceeds 100mA
- RoHS Compliant and Halogen Free

Package & Pin Assignment

GS74LVC1G07LF (SOT-23-5L)			GS74LVC1G07JCF (SOT-353)		
					
Pin	Pin Name	I/O	Description		
1	NC	--	Not connected		
2	A	I	Data input		
3	GND	--	Ground (0V)		
4	Y	O	Data output		
5	V _{cc}	--	Supply voltage		

Functional Block Diagram & Description

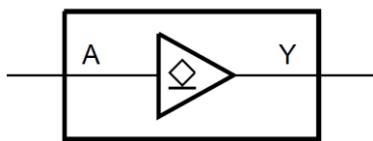


Fig 1. Function Diagram

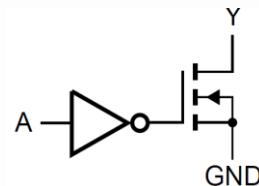


Fig 2. Logic Diagram

Input A	Output Y
L	L
H	Z

H = HIGH Voltage Level.

L = LOW Voltage Level.

Z = High-Impedance OFF-State.

Ordering and Marking Information

Ordering Information			
Part Number	Package	Part Marking	Quantity / Reel
GS74LVC1G07LF	SOT-23-5L	G07□□	3,000 PCS
GS74LVC1G07JCF	SOT-353	G07□□	3,000 PCS
GS74LVC1G107 1 2			
- Product GS74LVC1G07	- Package Code: 1 is L and JC L is SOT-23-5L JC is SOT-353	- Green Level: 2 is F for RoHS Compliant and Halogen Free	
Marking Information			
 <ul style="list-style-type: none"> - Product Code: G07 - GS Code: □□ 			

Absolute Maximum Ratings ($T_A=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Max.	Unit
Supply Voltage	V_{CC}	--	-0.5	+6.5	V
Input Voltage	V_I	[1]	-0.5	+6.5	V
Input Clamping Current	I_{IK}	$V_I < 0V$	-50	--	mA
Output Clamping Current	I_{OK}	$V_O < 0V$ or $V_O > V_{CC}$	-50	+50	mA
Output Voltage	V_O	Active Mode ^[1]	-0.5	$V_{CC}+0.5$	V
		Power-Down Mode ^[1]	-0.5	+6.5	V
Output Current	I_O	$V_O = 0V$ to V_{CC}	-	+50	mA
Supply Current	I_{CC}	--	--	+100	mA
Ground Current	I_{GND}	--	-100	--	mA
Storage Temperature	T_{stg}	--	-65	+150	$^\circ\text{C}$
Thermal Resistance Junction to Ambient	R_{thJA}	SOT-23-5L	229		$^\circ\text{C}/\text{W}$
		SOT-353	278		
Latch Up	LU	$T_A=25^\circ\text{C}$	100	--	mA

NOTE

1. The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

Recommended Operating Condition ($T_A=25^\circ\text{C}$ unless otherwise specified)

(Voltages are referenced to GND (ground=0V))

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Supply Voltage	V_{CC}	--	1.65	--	5.5	V
Input Voltage	V_I	--	0	--	5.5	V
Output Voltage	V_O	--	0	--	VCC	V
Ambient Temperature	T_A	--	-40	+25	+125	$^\circ\text{C}$
Input Transition rise and fall rate	$\Delta t/\Delta V$	$V_{CC} = 1.65V$ to $2.7V$	--	--	20	ns/V
		$V_{CC} = 2.7V$ to $5.5V$	--	--	10	ns/V

Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise specified)

■ Static Characteristics

(Voltages are referenced to GND (ground=0V))

Parameter	Symbol	Test Condition	-40°C to 85°C			-40°C to +125°C			Unit
			Min.	Typ. ^[1]	Max.	Min.	Max.		
High-level input voltage	V_{IH}	$V_{CC} = 1.65\text{V to } 1.95\text{V}$	0.65 V_{CC}	--	--	0.65 V_{CC}	--	--	V
		$V_{CC} = 2.3\text{V to } 2.7\text{V}$	1.7	--	--	1.7	--	--	V
		$V_{CC} = 2.7\text{V to } 3.6\text{V}$	2.0	--	--	2.0	--	--	V
		$V_{CC} = 4.5\text{V to } 5.5\text{V}$	0.7 V_{CC}	--	--	0.7 V_{CC}	--	--	V
Low-level input voltage	V_{IL}	$V_{CC} = 1.65\text{V to } 1.95\text{V}$	--	--	0.35 V_{CC}	--	0.35 V_{CC}	--	V
		$V_{CC} = 2.3\text{V to } 2.7\text{V}$	--	--	0.7	--	0.7	--	V
		$V_{CC} = 2.7\text{V to } 3.6\text{V}$	--	--	0.8	--	0.8	--	V
		$V_{CC} = 4.5\text{V to } 5.5\text{V}$	--	--	0.3 V_{CC}	--	0.3 V_{CC}	--	V
Low-level output voltage	V_{OL}	$V_I = V_{IH}$ or V_{IL}							
		$I_O = 100\mu\text{A}; V_{CC} = 1.65\text{V to } 5.5\text{V}$	--	--	0.1	--	0.1	--	V
		$I_O = 4\text{mA}; V_{CC} = 1.65\text{V}$	--	--	0.45	--	0.7	--	V
		$I_O = 8\text{mA}; V_{CC} = 2.3\text{V}$	--	--	0.30	--	0.45	--	V
		$I_O = 12\text{mA}; V_{CC} = 2.7\text{V}$	--	--	0.40	--	0.60	--	V
		$I_O = 24\text{mA}; V_{CC} = 3.0\text{V}$	--	--	0.55	--	0.80	--	V
		$I_O = 32\text{mA}; V_{CC} = 4.5\text{V}$	--	--	0.55	--	0.80	--	V
Input leakage current	I_I	$V_I = 5.5\text{V or GND}; V_{CC} = 0\text{V to } 5.5\text{V}$	--	± 0.1	± 1.0	--	± 1.0	--	μA
Power-off leakage current	I_{OFF}	$V_{CC} = 0\text{V}; V_I \text{ or } V_O = 5.5\text{V}$	--	± 0.1	± 2.0	--	± 2.0	--	μA
Supply current	I_{CC}	$V_I = 5.5\text{V or GND}; I_O = 0\text{A}; V_{CC} = 1.65\text{V to } 5.5\text{V}$	--	0.1	4.0	--	4.0	--	μA
Additional supply current	ΔI_{CC}	$V_{CC} = 2.3\text{V to } 5.5\text{V}$ $V_I = V_{CC} - 0.6\text{V}$ $I_O = 0\text{A}$ Per input pin;	--	5	500	--	500	--	μA
Input capacitance	C_I	--	--	5	--	--	--	--	pF

NOTE

- Typical values are measured at $V_{CC} = 3.3\text{V}$ and $T_A = 25^\circ\text{C}$

■ Dynamic Characteristics

(GND=0V. for test circuit see Fig.4)

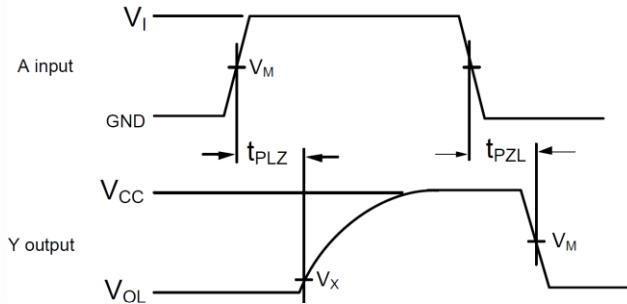
Characteristics	Symbol	Test condition	-40°C to 85°C			-40°C to +125°C			Unit
			Min.	Typ. ^[1]	Max.	Min.	Max.		
Propagation delay	t_{pd}	A to Y; see Fig.3 ^[2]							
		$V_{CC}=1.65V \text{ to } 1.95V$	1.0	5.2	10.8	1.0	13.2	ns	
		$V_{CC}=2.3V \text{ to } 2.7V$	0.5	3.0	7.5	0.5	9.0	ns	
		$V_{CC}=2.7V$	0.5	3.5	8.4	0.5	9.8	ns	
		$V_{CC}=3.0V \text{ to } 3.6V$	0.5	2.6	6.2	0.5	7.5	ns	
		$V_{CC}=4.5V \text{ to } 5.5V$	0.5	2.2	5.4	0.5	6.3	ns	

NOTE

1. Typical values are measured at $T_A=25^\circ\text{C}$, $V_{CC} = 1.8V, 2.5V, 3.3V$ and $5.0V$ respectively.

2. t_{pd} is the same as t_{PLZ} and t_{PZL} .

Waveforms and Test Circuit



V_{OL} are typical voltage output levels that occur with the output load.

Fig 3. Propagation delay input (A) to output (Y)

■ Measurement Points

Supply Voltage	Input		Output	
	V_M	V_M	V_M	V_x
1.65V to 1.95V	0.5 V_{CC}	0.5 V_{CC}	0.5 V_{CC}	$V_{OL}+0.15V$
2.3V to 2.7V	0.5 V_{CC}	0.5 V_{CC}	0.5 V_{CC}	$V_{OL}+0.15V$
2.7V	1.5V	1.5V	1.5V	$V_{OL}+0.30V$
3.0V to 3.6V	1.5V	1.5V	1.5V	$V_{OL}+0.30V$
4.5V to 5.5V	0.5 V_{CC}	0.5 V_{CC}	0.5 V_{CC}	$V_{OL}+0.30V$

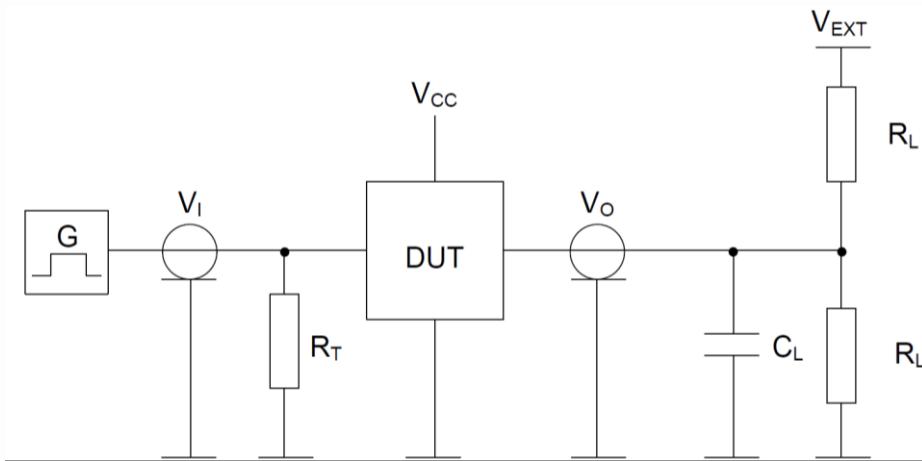


Fig 4. Test circuit for measuring switching times

Definitions test circuit :

C_L = Load capacitance including jig and probe capacitance.

R_T = Termination resistance should be equal to output impedance Z_0 of the pulse generator.

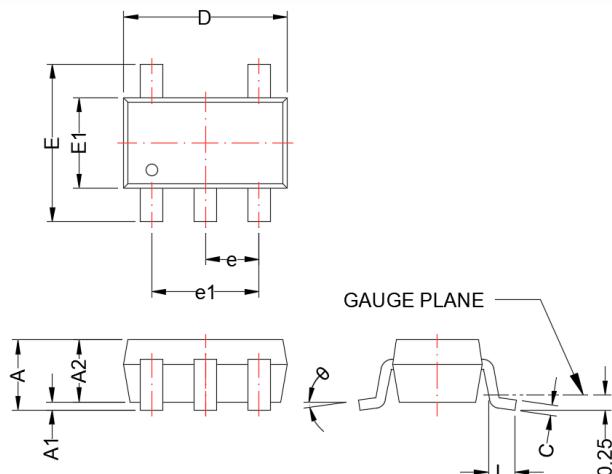
For t_{PLZ} , t_{PZL} , $S_1 = V_{CC}$

■ Test Data

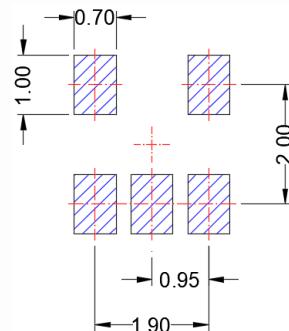
Supply voltage	Input		Load		V_{EXT}
	V_I	T_r, t_f	C_L	R_L	
1.65V to 1.95V	V_{CC}	$\leq 3.0\text{ns}$	30pF	1k Ω	2 V_{CC}
2.3V to 2.7V	V_{CC}	$\leq 3.0\text{ns}$	30pF	500 Ω	2 V_{CC}
2.7V	2.7V	$\leq 3.0\text{ns}$	50pF	500 Ω	6V
3.0V to 3.6V	2.7V	$\leq 3.0\text{ns}$	50pF	500 Ω	6V
4.5V to 5.5V	V_{CC}	$\leq 3.0\text{ns}$	50pF	500 Ω	2 V_{CC}

SOT-23-5L

Package Dimension



Recommended Land Pattern



Dimensions

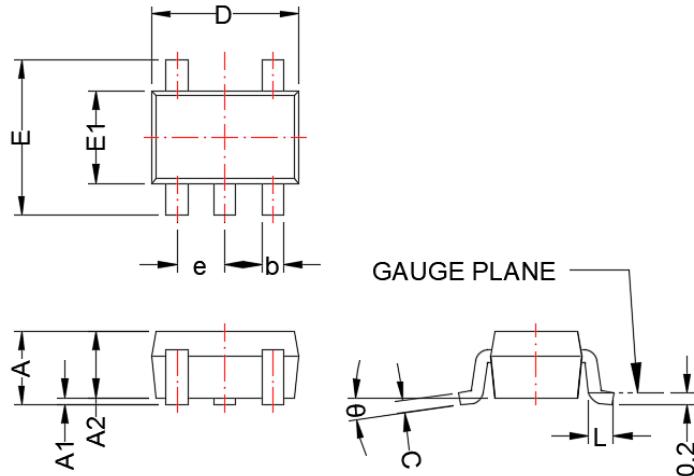
Symbol	Millimeters		Inches	
	Min	Max	Min	Max
A	0.90	1.45	0.035	0.057
A1	0.00	0.15	0.000	0.006
A2	0.90	1.30	0.035	0.051
b	0.30	0.50	0.012	0.020
c	0.08	0.26	0.003	0.010
D	2.70	3.10	0.106	0.122
E	2.20	3.00	0.087	0.118
E1	1.30	1.75	0.051	0.069
e	0.95 BSC		0.037 BSC	
e1	1.90 BSC		0.075 BSC	
L	0.30	0.60	0.012	0.024
θ	0°	8°	0°	8°

NOTE:

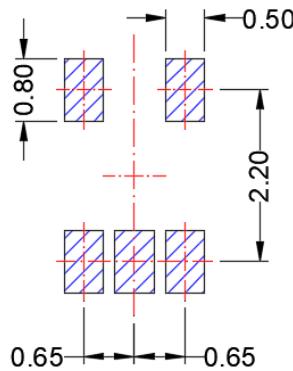
Dimensions are exclusive of Burrs, Mold Flash & Tie Bar extrusions.

SOT-353

Package Dimension



Recommended Land Pattern



Dimensions

Symbol	Millimeters		Inches	
	Min	Max	Min	Max
A	---	1.10	---	0.043
A1	0.00	0.10	0.000	0.004
A2	0.70	1.00	0.028	0.039
b	0.15	0.35	0.006	0.014
c	0.08	0.25	0.003	0.010
D	1.80	2.20	0.071	0.087
E	1.80	2.45	0.071	0.096
E1	1.15	1.35	0.045	0.053
e	0.65 BSC		0.026 BSC	
L	0.26	0.46	0.010	0.018
θ	0°	8°	0°	8°

NOTE:

Dimensions are exclusive of Burrs, Mold Flash & Tie Bar extrusions.

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