GSM3350AXF

30V N-Channel MOSFET

Product Description

The N-Channel enhancement mode power field effect transistor is using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

The device is well suited for high efficiency fast switching applications.

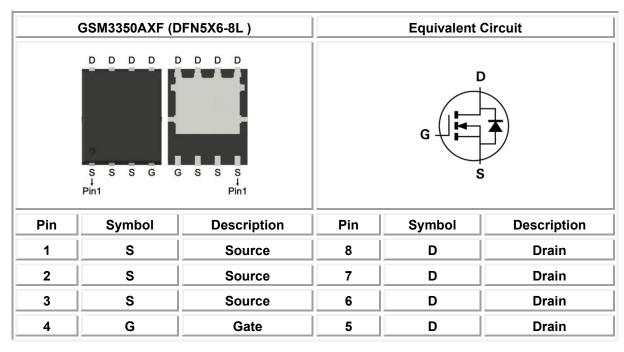
Features

- $R_{DS(ON)} = 5m\Omega$ @ $V_{GS}=10V$
- \blacksquare R_{DS(ON)} = 7m Ω @ V_{GS}=4.5V
- DFN5X6-8L Package
- RoHS Compliant and Halogen Free

Applications

- MB / VGA / Vcore
- POL Applications
- SMPS

Packages & Pin Assignments





Ordering and Marking Information

Ordering Information				
Part Number	Package Part Marking Quant		Quantity / Reel	
GSM3350AXF	DFN5X6-8L	DFN5X6-8L 3350AXF		
GSM3350A 1 2				
- Product Code: GSM3350A	- Package Code: 1 is X for DFN5X6-8L - Green Level: 2 is F for RoHS Compliant and Halogen Free			
	Marking Ir	formation		
3350AXF	- Product Cod 3350AXF	e:		
	- GS Code:			



Absolute Maximum Ratings (T_J = 25°C Unless otherwise noted)

Symbol	Parameter		Value	Unit
V _{DSS}	Drain-Source Voltage		30	V
V _{GSS}	Gate-Source Voltage		±20	V
	0 11 0 11	Tc=25°C	75	
lσ	Continuous Drain Current 1,	Tc=100°C	45	A
I _{DM}	Pulsed Drain Current		150	Α
I _{AS}	Single Pulse Avalanche Current, L = 0.5mH		15	Α
E _{AS}	Single Pulse Avalanche Energy, L = 0.5mH		168	mJ
	Danier Diagination 12	Tc=25°C	45	,,,,
P _D	Power Dissipation ^{1, 2}	Tc=100°C	18	W
R _{eJC}	Thermal Resistance-Junction to Case		2.8	°C/W
TJ	Operating Junction Temperature Range		-55 to +150	°C
Tstg	Storage Temperature Range		-55 to +150	°C

NOTE:

Electrical Characteristics (Tc=25°C Unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
	Static	Characteristics				
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250µA	30	-	-	V
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS} = V_{GS} , I_D =250 μ A	1.2	-	2.5	V
I _{GSS}	Gate-Source Leakage Current	V _{DS} =0V, V _{GS} =±20V	-	-	±100	nA
I _{DSS}	Drain-Source Leakage Current	V _{DS} =30V, V _{GS} =0V	-	-	1	μA
D		V _{GS} =10V, I _D =20A	-	3.8	5	mΩ
R _{DS(ON)}	Drain-Source On-Resistance	V _{GS} =4.5V, I _D =15A	-	5.6	7	
VsD	Diode Forward Voltage	V _{GS} =0V, I _S =20A	-	-	1.2	V
	Dynam	nic Characteristics				
Ciss	Input Capacitance		-	2461	-	
Coss	Output Capacitance	V _{DS} =15V, V _{GS} =0V, f=1MHz	-	383	-	pF
C _{rss}	Reverse Transfer Capacitance		-	298	-	
Qg	Total Gate Charge		-	46	-	
Qgs	Gate-Source Charge	V _{DS} =15V, V _{GS} =10V,	-	11.4	-	nC
Q _{gd}	Gate-Drain Charge	1D-20A	-	8.2	-	i
t _{d(on)}	Turn-On Delay Time		-	12.6	-	
t _r	Turn-On Rise Time	V _{DS} =15V, V _{GS} =10V,	-	12	-	i
t _{d(off)}	Turn-Off Delay Time	Rg=3.3Ω, I _D =20A	-	55	-	ns
t _f	Turn-Off Fall Time		-	15.6	-	i



^{1.}The maximum current rating is limited by P_D.
2.The data tested by surface mounted on a 1 inch2 FR-4 board with 2oz copper.

Typical Performance Characteristics

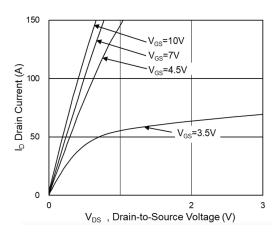


Fig.1 Output Characteristics

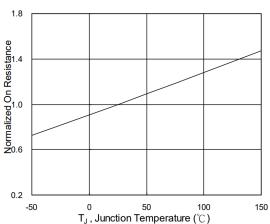


Fig.3 Normalized On-Resistance vs. T_J

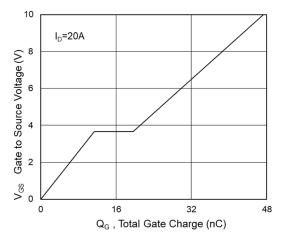


Fig.5 Gate Charge Characteristics

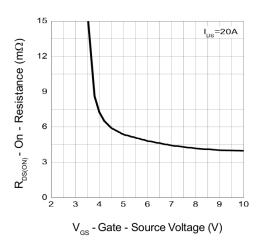


Fig.2 On-Resistance vs. Gate Voltage

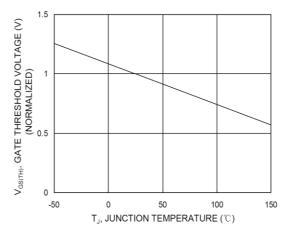


Fig.4 Normalized V_{GS(th)} vs. T_J

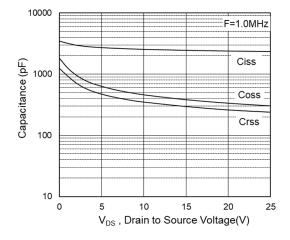


Fig.6 Capacitance Characteristics



Typical Performance Characteristics

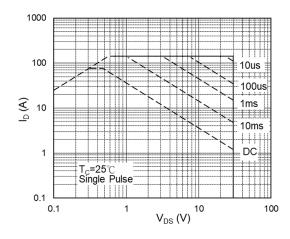


Fig.7 Maximum Safe Operation Area

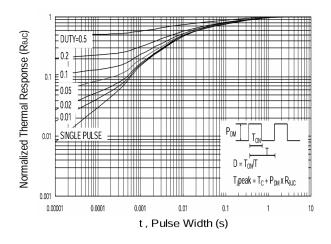


Fig.8 Normalized Transient Impedance

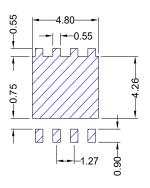


DFN5X6-8L

Package Dimension

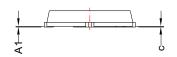
Pin1 BACKSIDE VIEW

Recommended Land Pattern



Unit: mm





	Dimensions				
Symbol	Millimeters		Inches		
	MIN	MAX	MIN	MAX	
Α	2.18	2.40	0.086	0.094	
A 1	0.00	0.15	0.000	0.006	
b	0.64	0.90	0.025	0.035	
С	0.40	0.89	0.016	0.035	
с1	0.40	0.61	0.016	0.024	
D	6.35	6.73	0.250	0.265	
D1	4.95	5.46	0.195	0.215	
D2	4.32	-	0.170	-	
E	9.40	10.41	0.370	0.410	
E1	5.97	6.22	0.235	0.245	
E2	4.95	-	0.195	-	
е	2.286 BSC		0.090 BSC		
L	1.40	1.77	0.055	0.070	
L1	2.67	3.07	0.105	0.121	
θ	0°	8°	0°	8°	

NOTE

Dimensions are exclusive of Burrs, Mold Flash and Tie Bar extrusions.



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