

# GSM2230JRF

## 20V Dual N-Channel MOSFETs

### Product Description

The N-Channel enhancement mode power field effect transistor is using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

The device is well suited for high efficiency fast switching applications.

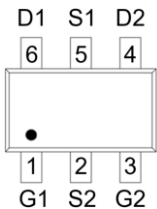
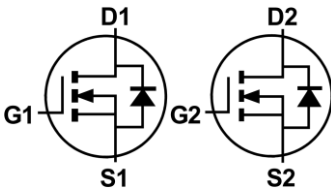
### Features

- $R_{DS(ON)}=28m\Omega@V_{GS}=4.5V$
- $R_{DS(ON)}=35m\Omega@V_{GS}=2.5V$
- $R_{DS(ON)}=55m\Omega@V_{GS}=1.8V$
- TSOT-23-6L Package
- RoHS Compliant and Halogen Free

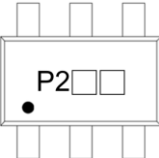
### Applications

- Portable Equipment
- Battery Powered System
- Net Working System

### Packages & Pin Assignments

TSOT-23-6L			Equivalent Circuit		
					
Pin	Symbol	Description	Pin	Symbol	Description
1	G1	Gate 1	6	D1	Drain 1
2	S2	Source 2	5	S1	Source 1
3	G2	Gate 2	4	D2	Drain 2

## Ordering and Marking Information

Ordering Information			
Part Number	Package	Part Marking	Quantity / Reel
GSM2230JRF	TSOT-23-6L	P2□□	3,000 PCS
<b>GSM2230</b> <span style="border: 1px solid black; padding: 0 2px;">1</span> <span style="border: 1px solid black; padding: 0 2px;">2</span>			
- <b>Product Code:</b> GSM2230		- <b>Package Code:</b> <span style="border: 1px solid black; padding: 0 2px;">1</span> is <b>JR</b> for TSOT-23-6L	
		- <b>Green Level:</b> <span style="border: 1px solid black; padding: 0 2px;">2</span> is <b>F</b> for RoHS Compliant and Halogen Free	
Marking Information			
		- <b>Product Code:</b> P2	
		- <b>GS Code:</b> □□	

## Absolute Maximum Ratings (T<sub>A</sub> = 25°C unless otherwise specified)

Symbol	Parameter	Value	Unit
V <sub>DSS</sub>	Drain-Source Voltage	20	V
V <sub>GSS</sub>	Gate-Source Voltage	±12	V
I <sub>D</sub>	Continuous Drain Current	T <sub>A</sub> =25°C	6
		T <sub>A</sub> =70°C	4.8
I <sub>DM</sub>	Pulsed Drain Current <sup>1</sup>	20	A
P <sub>D</sub>	Power Dissipation	T <sub>A</sub> =25°C	1.52
		T <sub>A</sub> =70°C	0.97
R <sub>θJA</sub>	Thermal Resistance-Junction to Ambient <sup>2</sup>	82	°C/W
T <sub>J</sub>	Operating Junction Temperature Range	-55 to +150	°C
T <sub>STG</sub>	Storage Temperature Range	-55 to +150	°C

### NOTE:

- Single pulse width is limited by max junction temperature.
- The device was mounted on 1in<sup>2</sup> FR-4 board with 2oz.copper.

## Electrical Characteristics (T<sub>J</sub> = 25°C unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
B <sub>V</sub> DSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	20	-	-	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =20V, V <sub>GS</sub> =0V	-	-	1	μA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> =±12V	-	-	±100	nA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	0.4	-	1	V
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> =4.5V, I <sub>D</sub> =4A	-	22	28	mΩ
		V <sub>GS</sub> =2.5V, I <sub>D</sub> =3A	-	30	35	
		V <sub>GS</sub> =1.8V, I <sub>D</sub> =2A	-	43	55	
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =10V, I <sub>D</sub> =3A	-	5	-	S
<b>Dynamic Characteristics</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =10V, V <sub>GS</sub> =0V, f=1MHz	-	490	-	pF
C <sub>oss</sub>	Output Capacitance		-	86	-	
C <sub>rss</sub>	Reverse Transfer Capacitance		-	70	-	
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =10V, I <sub>D</sub> =4A V <sub>GS</sub> =4.5V	-	6.2	-	nC
Q <sub>gs</sub>	Gate-Source Charge		-	0.5	-	
Q <sub>gd</sub>	Gate-Drain Charge		-	1.4	-	
t <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> =10V, I <sub>D</sub> =4A V <sub>GS</sub> =4.5V, R <sub>g</sub> =6Ω	-	8	-	ns
t <sub>r</sub>	Turn-On Rise Time		-	35	-	
t <sub>d(off)</sub>	Turn-Off Delay Time		-	39	-	
t <sub>f</sub>	Turn-Off Fall Time		-	14	-	
<b>Diode Characteristics</b>						
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =1A	-	-	1	V

## Typical Performance Characteristics

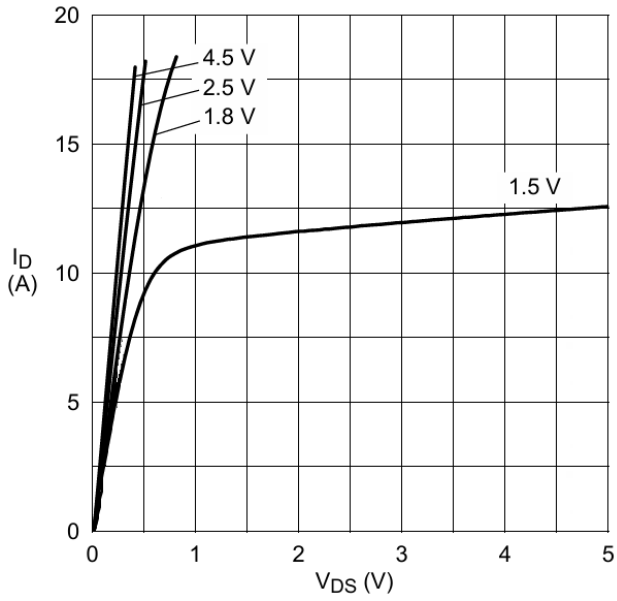


FIG.1 Output Characteristics

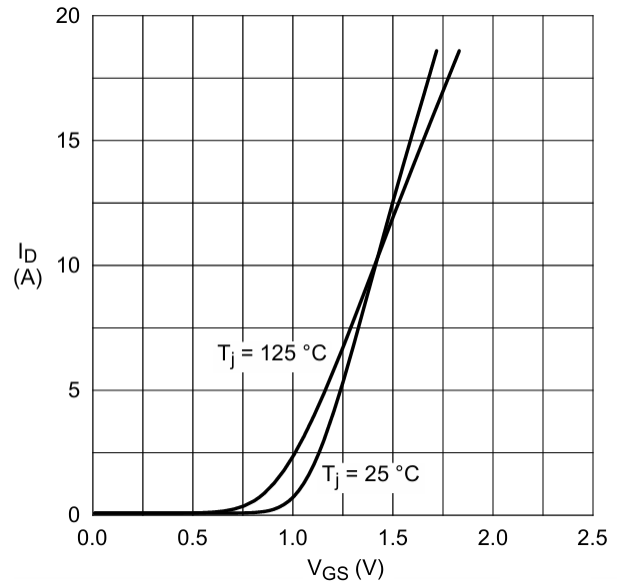


FIG.2 Transfer Characteristics

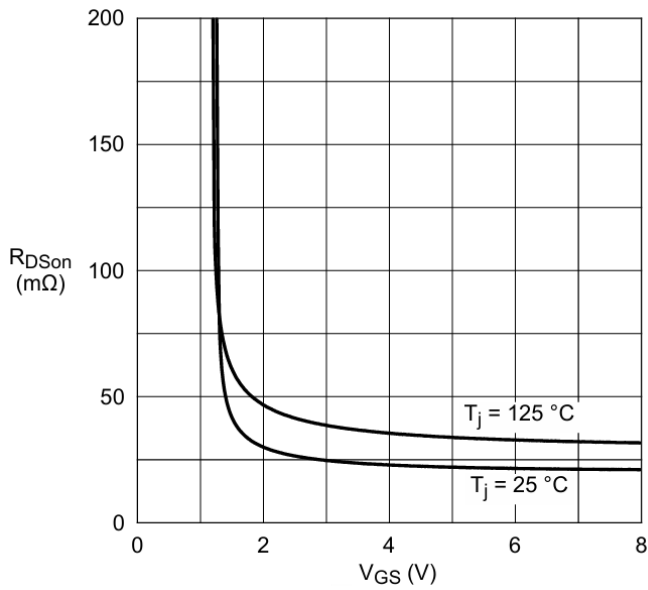


FIG.3 On-Resistance vs. Gate Voltage

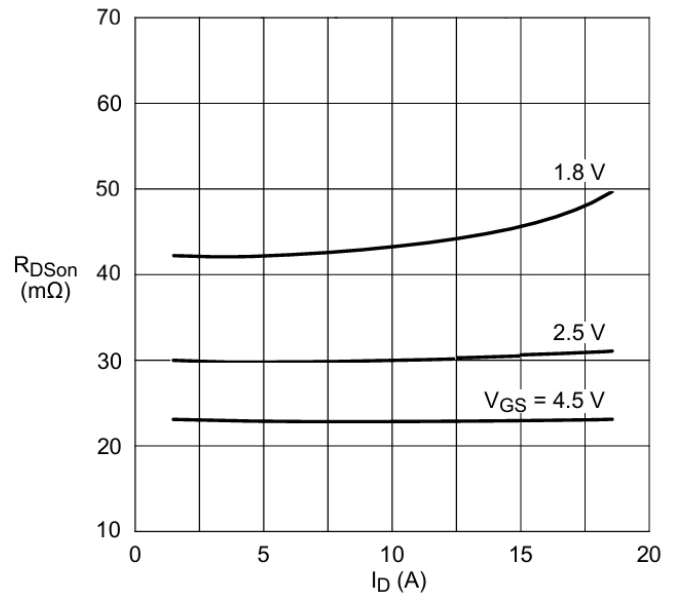


FIG.4 On-Resistance vs. Drain Current

## Typical Performance Characteristics

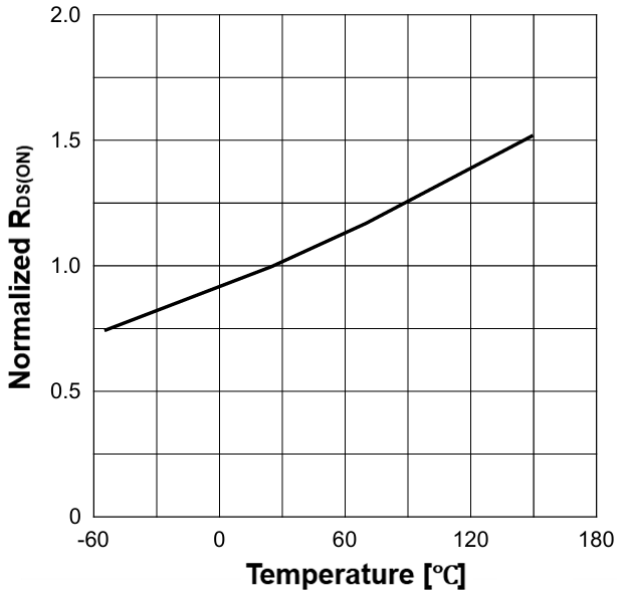


FIG.5 Normalized On-Resistance vs.  $T_J$

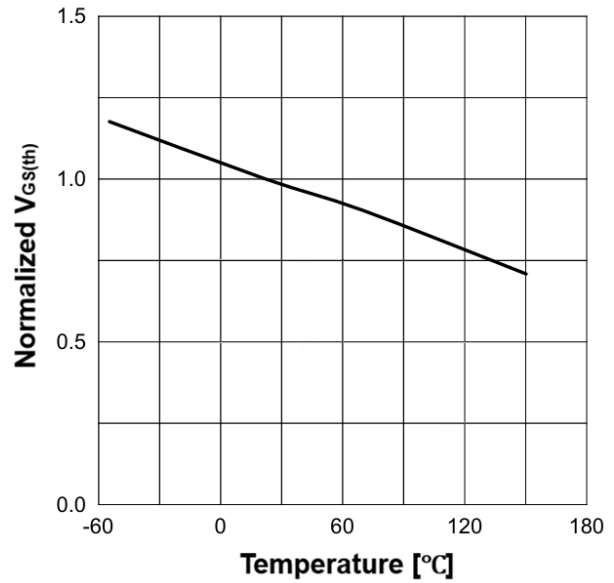


FIG.6 Normalized  $V_{GS(th)}$  vs.  $T_J$

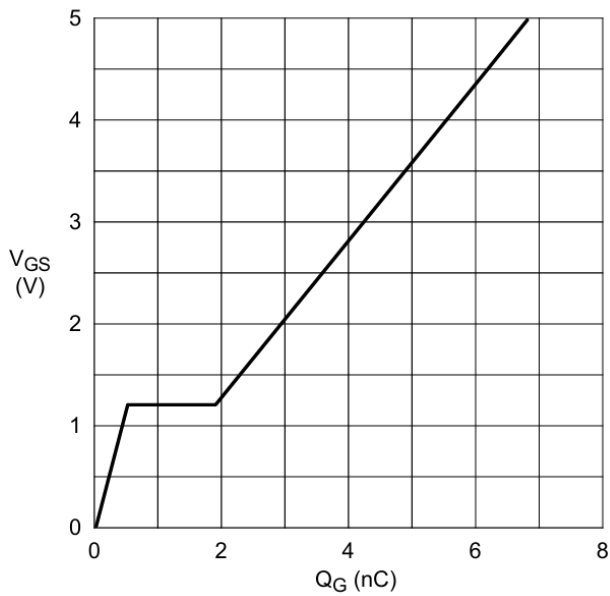


FIG.7 Gate Charge Characteristics

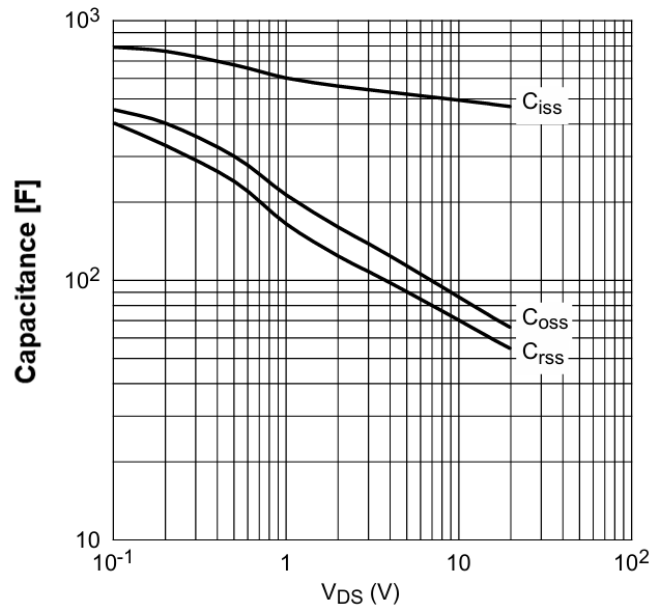


FIG.8 Capacitance Characteristics

## Typical Performance Characteristics

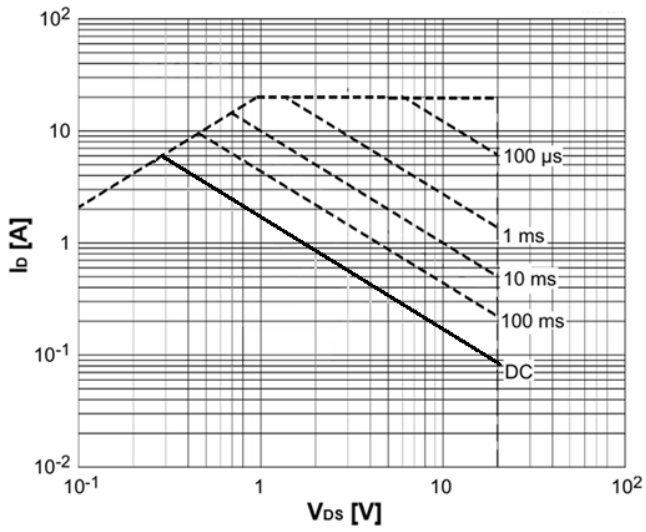


FIG.9 Maximum Safe Operation Area

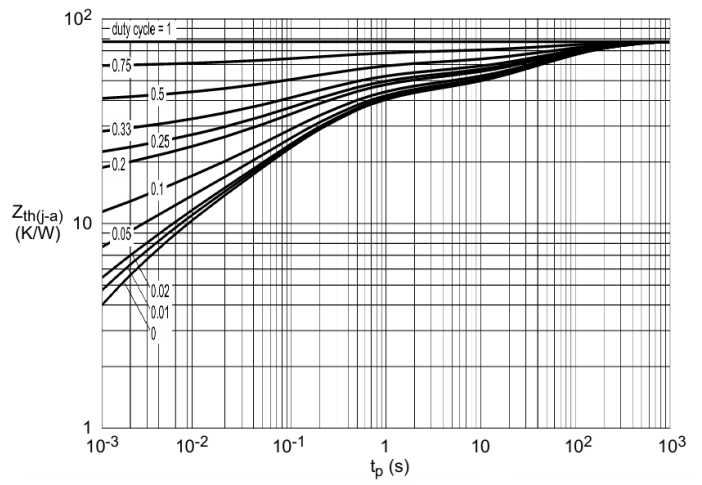


FIG.10 Typical Transient Impedance  $Z_{\theta JA}$

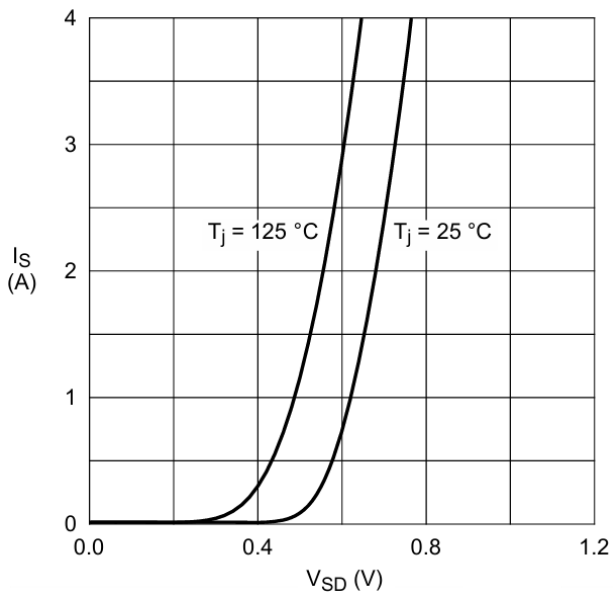
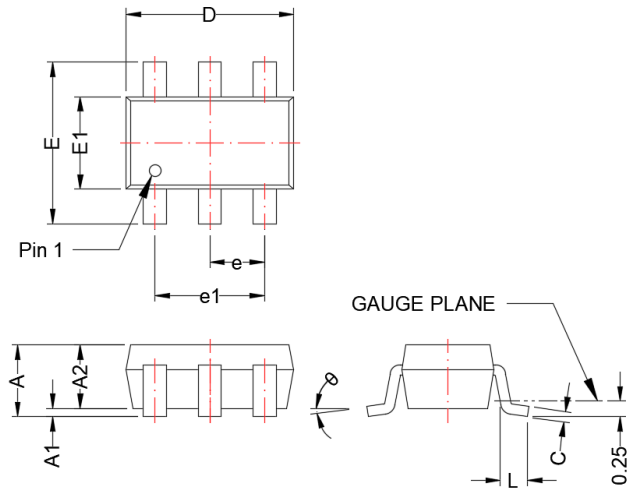


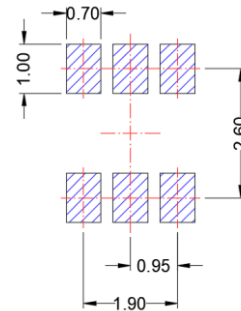
FIG.11 Body-Diode Characteristics

# TSOT-23-6L

## Package Dimension (2)



## Recommended Land Pattern



Unit:mm

Dimensions				
Symbol	Millimeters		Inches	
	MIN	MAX	MIN	MAX
A	---	1.10	---	0.043
A1	0.00	0.10	0.000	0.004
A2	0.70	1.00	0.028	0.039
b	0.30	0.50	0.012	0.020
c	0.08	0.20	0.003	0.008
D	2.70	3.10	0.106	0.122
E	2.20	3.00	0.087	0.118
E1	1.30	1.75	0.051	0.069
e	0.95 BSC		0.037 BSC	
e1	1.90 BSC		0.075 BSC	
L	0.3	0.6	0.012	0.024
θ	0°	8°	0°	8°





**NOTE:**



Dimensions are exclusive of Burrs, Mold Flash and Tie Bar extrusions.

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