

# GSMDL6910

## 60V N-Channel MOSFETs

### Product Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

These devices are well suited for high efficiency fast switching applications.


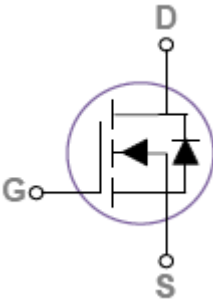
### Features

- 60V, 6.8A,  $R_{DS(ON)}=60m\Omega@V_{GS}=10V$
- Improved dv/dt capability
- Fast switching
- 100% EAS guaranteed
- Green Device Available
- SOT-223 package design

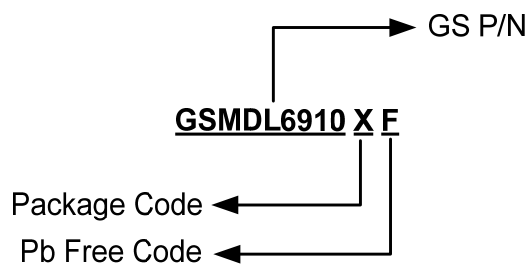
### Applications

- Motor Drive
- Power Tools
- LED Lighting

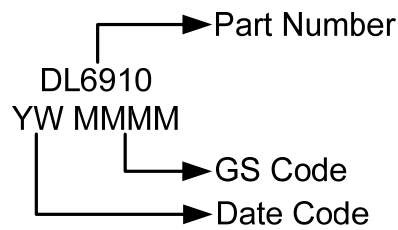
### Packages & Pin Assignments

GSMDL6910XF (SOT-223)	
	
	
Pin	Description
1	Gate
2	Drain
3	Source

### Ordering Information



## Marking Information



Part Number	Package	Quantity
GSMDL6910XF	SOT-223	2500 PCS

## Absolute Maximum Ratings

$T_c=25^\circ\text{C}$  Unless otherwise noted

Symbol	Parameter	Typical	Unit
$V_{DS}$	Drain-Source Voltage	60	V
$V_{GS}$	Gate -Source Voltage	$\pm 20$	V
$I_D$	Continuous Drain Current	$T_c=25^\circ\text{C}$	6.8
		$T_c=100^\circ\text{C}$	4.3
$I_{DM}$	Pulsed Drain Current (*1)	27.2	A
EAS	Single Pulse Avalanche Energy (*2)	11	mJ
IAS	Single Pulse Avalanche Current (*2)	15	A
$P_D$	Power Dissipation ( $T_c=25^\circ\text{C}$ )	5.4	W
	Power Dissipation (Derate above $25^\circ\text{C}$ )	0.043	W/ $^\circ\text{C}$
$T_J$	Operating Junction Temperature Range	-50 to +150	$^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-50 to +150	$^\circ\text{C}$
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	85	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance-Junction to Case	23	$^\circ\text{C}/\text{W}$

Note 1: Repetitive Rating: Pulsed width limited by maximum junction temperature.

Note 2:  $V_{DD}=25\text{V}$ ,  $V_{GS}=10\text{V}$ ,  $L=0.1\text{mH}$ ,  $I_{AS}=15\text{A}$ , Starting  $T_J=25^\circ\text{C}$ .

## Electrical Characteristics

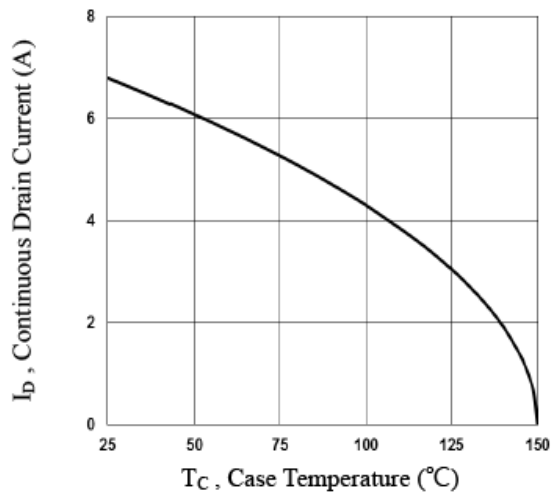
T<sub>J</sub>=25°C Unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Static</b>						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	60			V
ΔBV <sub>DSS</sub> /ΔT <sub>J</sub>	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25°C, I <sub>D</sub> =1mA		0.05		V/°C
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	1.2	1.8	2.5	V
ΔV <sub>GS(th)</sub>	V <sub>GS(th)</sub> Temperature Coefficient			-4.2		mV/°C
I <sub>GSS</sub>	Gate Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V			1	uA
		V <sub>DS</sub> =48V, V <sub>GS</sub> =0V, T <sub>J</sub> =125°C			10	
I <sub>S</sub>	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current			6.8	A
I <sub>SM</sub>	Pulsed Source Current				13.6	
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =6A		50	60	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =3A		56	70	
g <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> =10V, I <sub>D</sub> =4A		10		S
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =1A			1	V
t <sub>rr</sub>	Reverse Recovery Time (*3)	V <sub>GS</sub> =30V, I <sub>S</sub> =1A, di/dt=100A/us		14.6		ns
Q <sub>rr</sub>	Reverse Recovery Charge (*3)			6.6		nC
<b>Dynamic</b>						
Q <sub>g</sub>	Total Gate Charge (*3,4)	V <sub>DS</sub> =48V, V <sub>GS</sub> =10V, I <sub>D</sub> =6A		14	21	nC
Q <sub>gs</sub>	Gate-Source Charge (*3,4)			2.9	5	
Q <sub>gd</sub>	Gate-Drain Charge (*3,4)			2.4	4	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1MHz		835	1300	pF
C <sub>oss</sub>	Output Capacitance			69	130	
C <sub>rss</sub>	Reverse Transfer Capacitance			40	80	
t <sub>d(on)</sub>	Turn-On Time (*3,4)	V <sub>DD</sub> =30V, I <sub>D</sub> =6A, V <sub>GS</sub> =10V, R <sub>G</sub> =6Ω		14	27	ns
t <sub>r</sub>				4	8	
t <sub>d(off)</sub>	Turn-Off Time (*3,4)			32	60	
t <sub>f</sub>				2	4	
R <sub>g</sub>	Gate Resistance		V <sub>DS</sub> =0V, V <sub>GS</sub> =0V, f=1MHz		1.7	

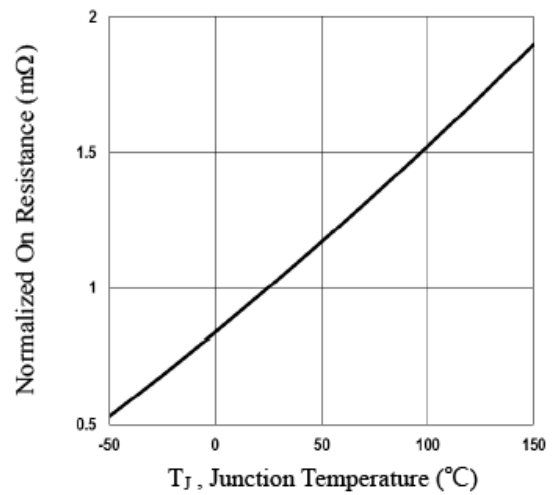
Note 3: The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.

Note 4: Essentially independent of operating temperature.

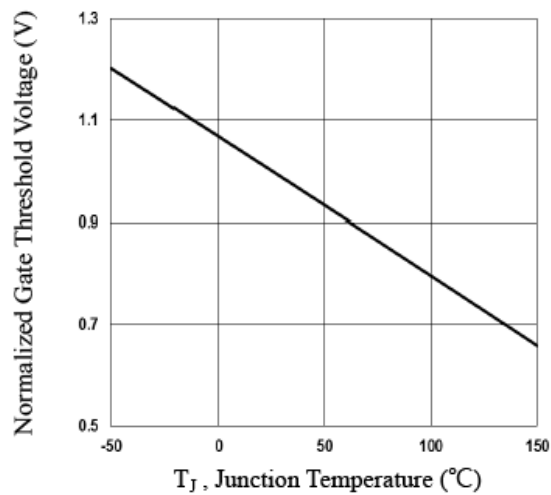
## Typical Performance Characteristics



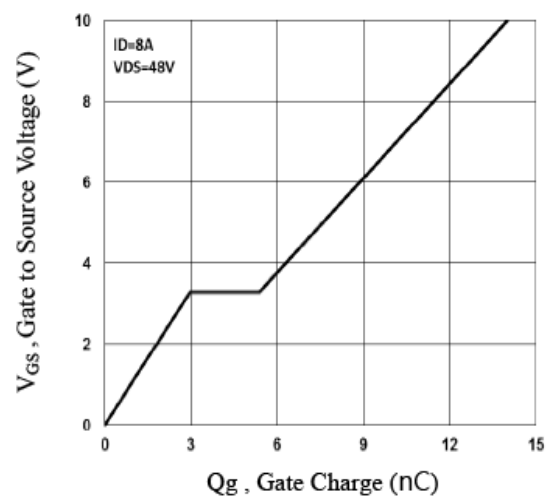
**Fig.1 Continuous Drain Current vs.  $T_c$**



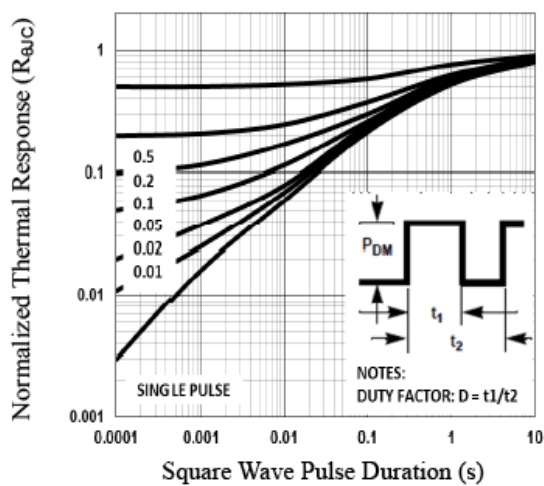
**Fig.2 Normalized  $R_{DS(on)}$  vs.  $T_j$**



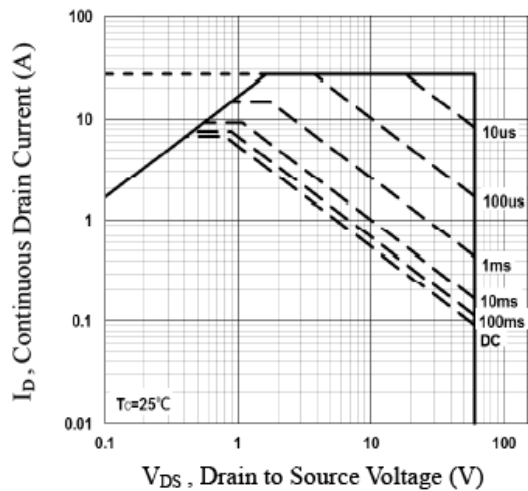
**Fig.3 Normalized  $V_{th}$  vs.  $T_j$**



**Fig.4 Gate Charge Waveform**



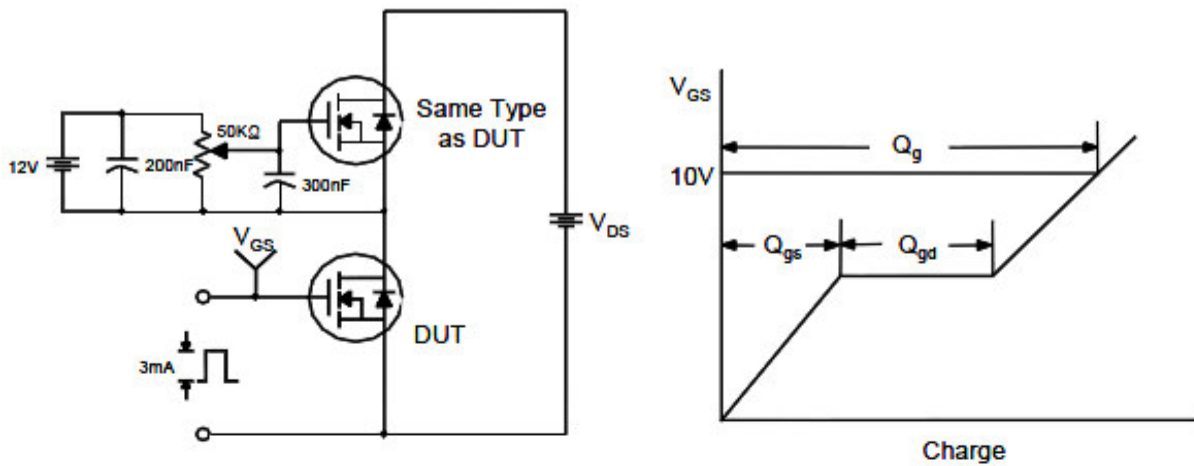
**Fig.5 Normalized Transient Impedance**



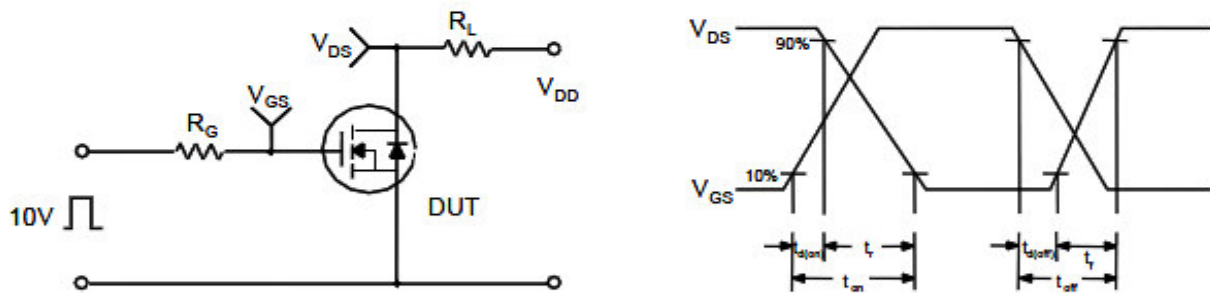
**Fig.6 Maximum Safe Operation Area**

## Typical Performance Characteristics (Continue)

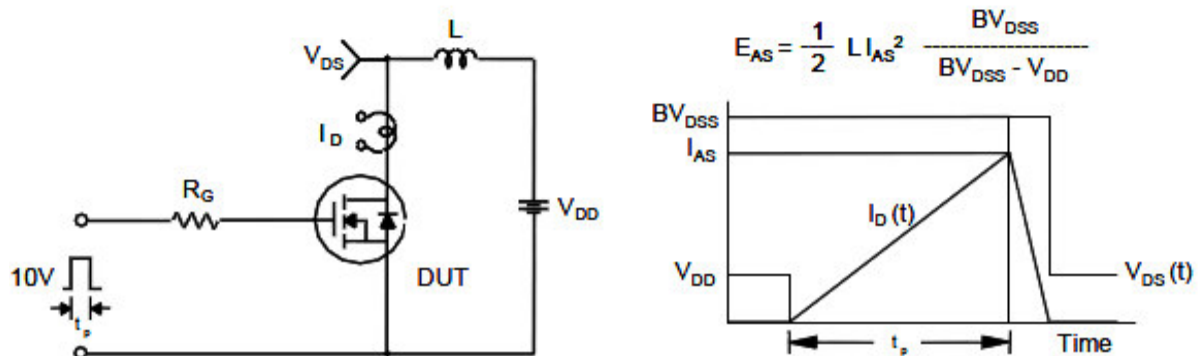
### Gate Charge Test Circuit & Waveform



### Resistive Switching Test Circuit & Waveforms

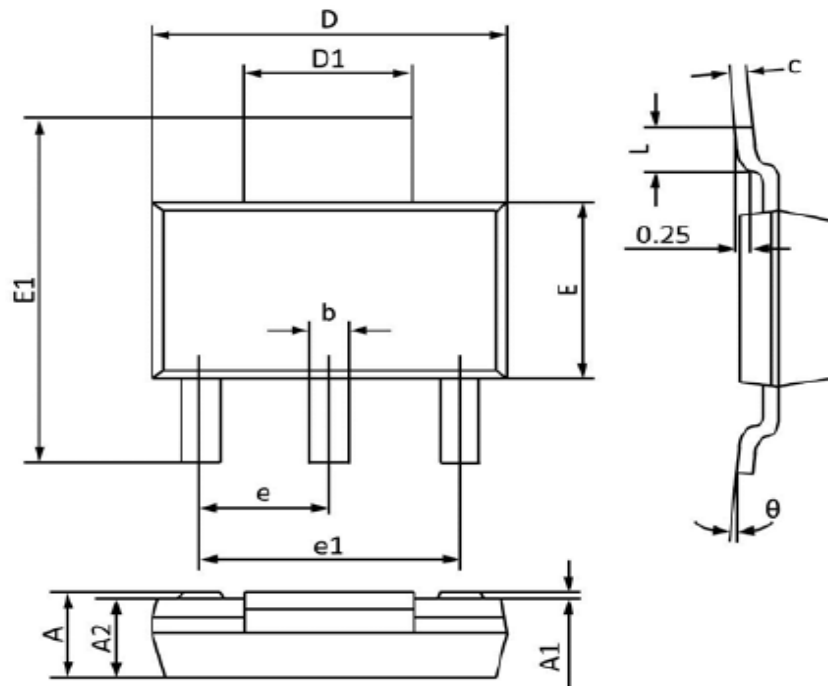


### Unclamped Inductive Switching Test Circuit & Waveforms



## Package Dimension

### SOT-223










Dimensions				
Symbol	Millimeters		Inches	
	Min	Max	Min	Max
A	1.520	1.800	0.060	0.071
A1	0.000	0.100	0.000	0.004
A2	1.500	1.700	0.059	0.067
b	0.660	0.820	0.026	0.032
c	0.250	0.350	0.010	0.014
D	6.200	6.400	0.244	0.252
D1	2.900	3.100	0.114	0.122
E	3.300	3.700	0.130	0.146
E1	6.830	7.070	0.269	0.278
e	2.300 (BSC)		0.091 (BSC)	
e1	4.500	4.700	0.177	0.185
L	0.900	1.150	0.035	0.045
θ	0°	10°	0°	10°



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