

GSMDC30N15X

150V N-Channel MOSFETs

Product Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

These devices are well suited for high efficiency fast switching applications.

Features

- 150V, 15A, $R_{DS(ON)}=65m\Omega@V_{GS}=10V$
- Improved dv/dt capability
- Fast switching
- V_{GS} Guaranteed $\pm 25V$
- Green Device Available
- DFN5X6-8L package design

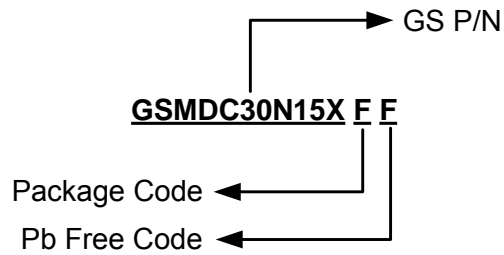
Applications

- Motor Drive
- Power Tools
- LED Lighting

Packages & Pin Assignments

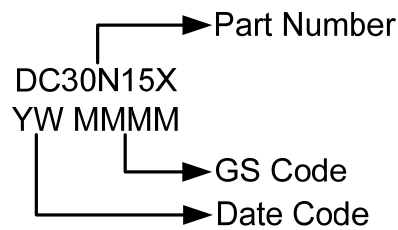
GSMDC30N15XFF (DFN5X6-8L)	
<p style="text-align: center;">Bottom View</p>	
Pin	Description
1	Source
2	Source
3	Source
4	Gate
5	Drain
6	Drain
7	Drain
8	Drain

Ordering Information



Part Number	Package
GSMDC30N15XFF	DFN5X6-8L

Marking Information



Absolute Maximum Ratings

T_A=25°C Unless otherwise noted

Symbol	Parameter	Typical	Unit
V _{DS}	Drain-Source Voltage	150	V
V _{GS}	Gate-Source Voltage	±25	V
I _D	Continuous Drain Current	T _A =25°C	15
		T _A =100°C	9.5
I _{DM}	Pulsed Drain Current	60	A
P _D	Power Dissipation (T _A =25°C)	135	W
	Power Dissipation (Derate above 25°C)	1.08	W/°C
T _J	Operating Junction Temperature Range	-55 to +150	°C
T _{STG}	Storage Temperature Range	-55 to +150	°C
R _{θJA}	Thermal Resistance-Junction to Ambient	62	°C/W
R _{θJC}	Thermal Resistance-Junction to Case	0.92	°C/W

Electrical Characteristics

T_A=25°C Unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	150			V
ΔBV _{DSS} /ΔT _J	BV _{DSS} Temperature Coefficient	Reference to 25°C, I _D =1mA		0.08		V/°C
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	2	3	4	V
ΔV _{GS(th)}	V _{GS(th)} Temperature Coefficient			-2.5		mV/°C
I _{GSS}	Gate Leakage Current	V _{DS} =0V, V _{GS} =±25V			±100	nA
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =120V, V _{GS} =0V			1	μA
		V _{DS} =96V, V _{GS} =0V, T _J =125°C			30	
I _S	Continuous Source Current	V _G =V _D =0V, Force Current			25	A
I _{SM}	Pulsed Source Current				50	
R _{DS(on)}	Drain-Source On-Resistance	V _{GS} =10V, I _D =6A		52	65	mΩ
		V _{GS} =6V, I _D =4A		60	80	
V _{SD}	Diode Forward Voltage	V _{GS} =0V, I _S =1A			1.2	V
Dynamic						
Q _g	Total Gate Charge	V _{DS} =30V, V _{GS} =10V, I _D =4A		30	45	nC
Q _{gs}	Gate-Source Charge			8.7	14	
Q _{gd}	Gate-Drain Charge			8	15	
C _{iss}	Input Capacitance	V _{DS} =30V, V _{GS} =0V, f=1MHz		1790	3000	pF
C _{oss}	Output Capacitance			160	300	
C _{rss}	Reverse Transfer Capacitance			82	160	
t _{d(on)}	Turn-On Time	V _{DD} =30V, I _D =1A, V _{GS} =10V, R _G =6Ω		14.5	28	ns
t _r				19.2	18	
t _{d(off)}	Turn-Off Time			33.6	60	
t _f				22.8	25	
R _g	Gate Resistance	V _{DS} =0V, V _{GS} =0V, f=1MHz		1.4	2.8	Ω

Typical Performance Characteristics

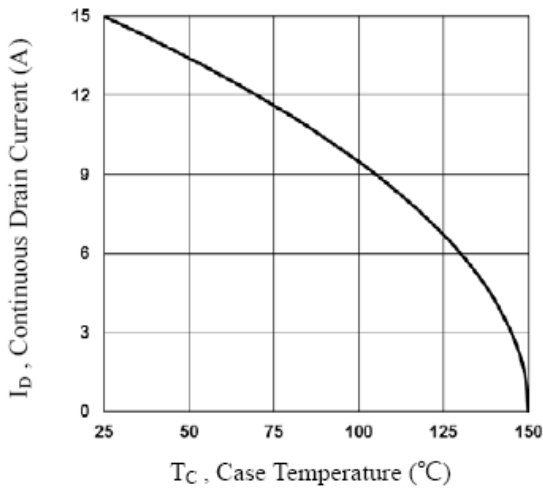


Fig.1 Continuous Drain Current vs. T_c

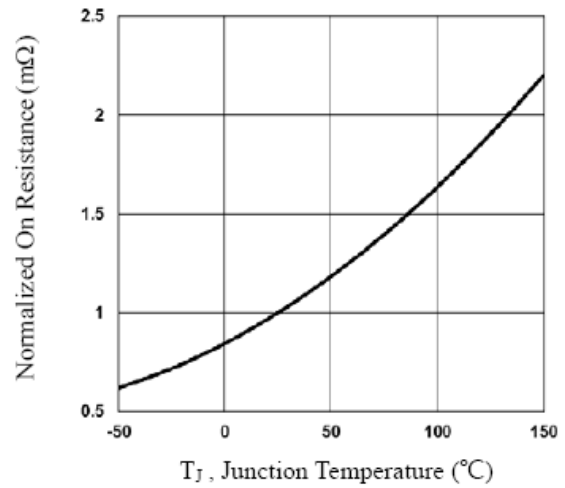


Fig.2 Normalized $R_{DS(on)}$ vs. T_j

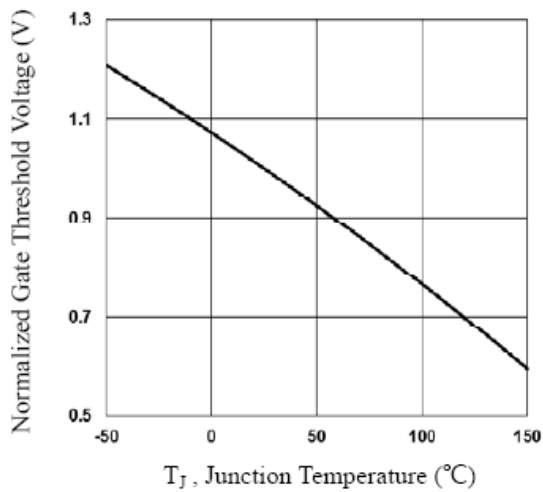


Fig.3 Normalized V_{th} vs. T_j

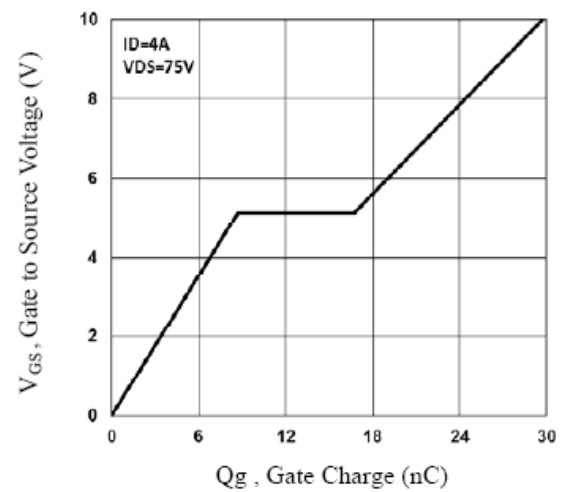


Fig.4 Gate Charge Characteristics

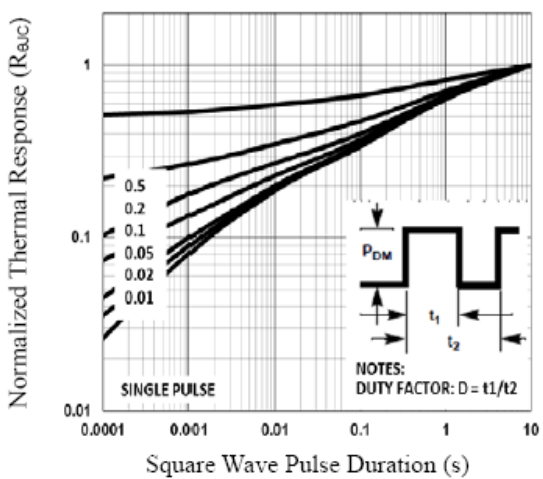


Fig.5 Normalized Transient Impedance

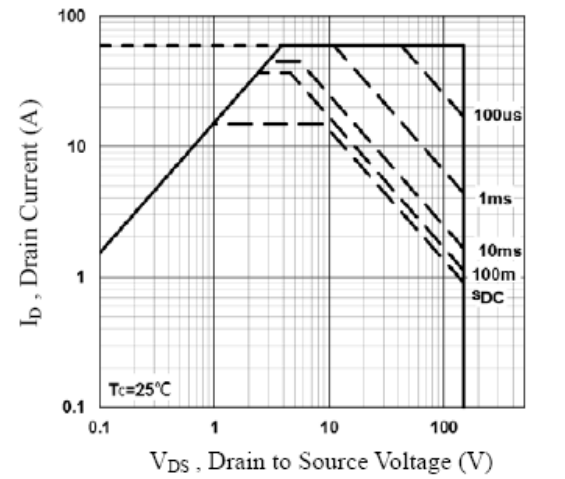
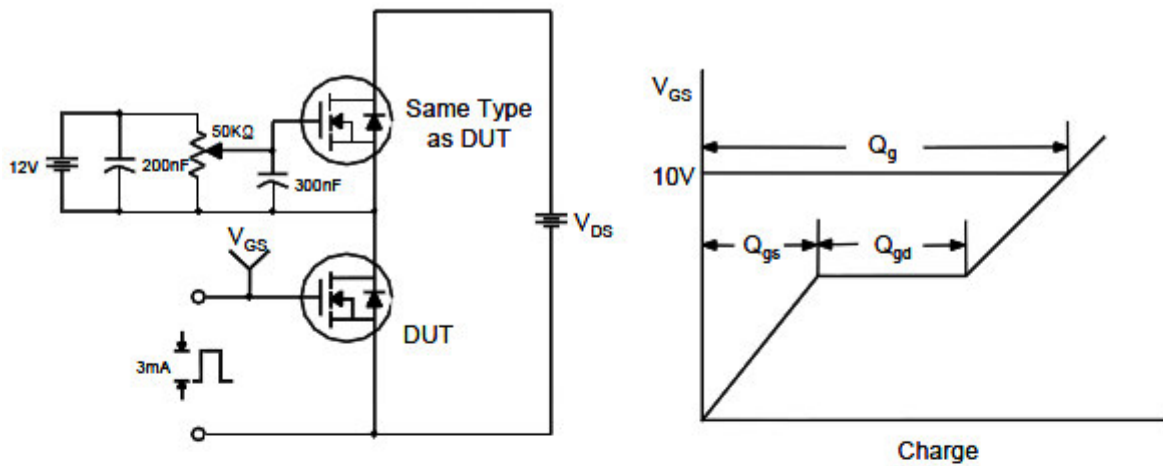


Fig.6 Maximum Safe Operation Area

Typical Performance Characteristics (Continue)

Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms

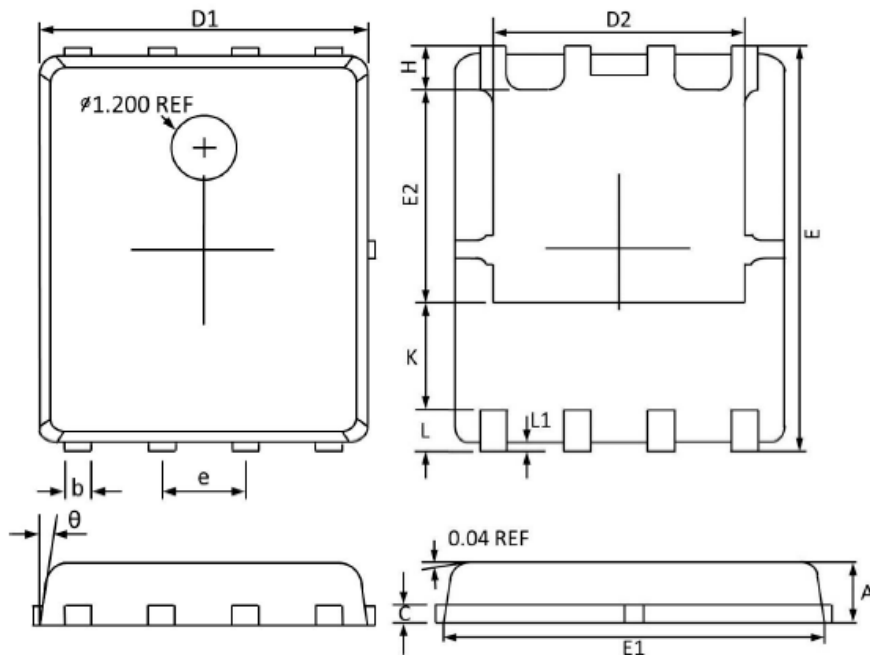


Unclamped Inductive Switching Test Circuit & Waveforms



Package Dimension

DFN5X6-8L



Dimensions

Symbol	Millimeters		Inches	
	Min	Max	Min	Max
A	0.800	1.100	0.031	0.043
b	0.330	0.510	0.013	0.020
c	0.200	0.300	0.008	0.012
D1	4.800	5.100	0.189	0.201
D2	3.610	4.100	0.142	0.161
E	5.900	6.200	0.232	0.244
E1	5.700	5.900	0.224	0.232
E2	3.350	3.780	0.132	0.149
e	1.270 (BSC)		0.050 (BSC)	
H	0.410	0.610	0.017	0.024
K	1.100	1.500	0.043	0.059
L	0.510	0.710	0.020	0.028
L1	0.060	0.200	0.002	0.008
θ	0°	12°	0°	12°

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